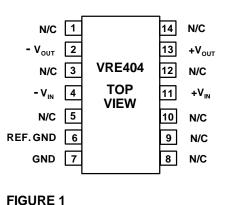


## **FEATURES**

- ±4.500 V OUTPUT ± 0.400 mV (.01%)
- TEMPERATURE DRIFT: 0.6 ppm/°C
- LOW NOISE: 3µVpp (0.1-10Hz)
- •TRACKING ERROR: 0.3 mV max.
- EXCELLENT LINE REGULATION: 6ppm/V Typ.
- SURFACE MOUNT AND DIP PACKAGES



**PIN CONFIGURATION** 

## DESCRIPTION

The VRE404 is a low cost, high precision,  $\pm 4.5V$  reference. Packaged in 14 pin DIP or SMT packages, the device is ideal for new designs that need a high performance reference.

The device provides ultrastable  $\pm 4.500V$  output with  $\pm 0.450$  mV (.01%) initial accuracy and a temperature coefficient of 0.6 ppm/°C. This improvement in accuracy is made possible by a unique, patented multipoint laser compensation technique developed by Thaler Corporation.

Another key feature of this reference is the 0.3 mV maximum tracking error between the positive and negative output voltages over the operating temperature range. This is extremely important in high performance systems for reducing overall system errors.

For designs which use the DIP package in a socket, there is a reference ground pin to eliminate reference ground errors.

# The VRE404 is recommended for use as a reference for high precision D/A and A/D converters which require an external precision reference. The device is ideal for calibrating scale factor on high resolution A/D converters. The VRE404 offers superior performance over monolithic references.

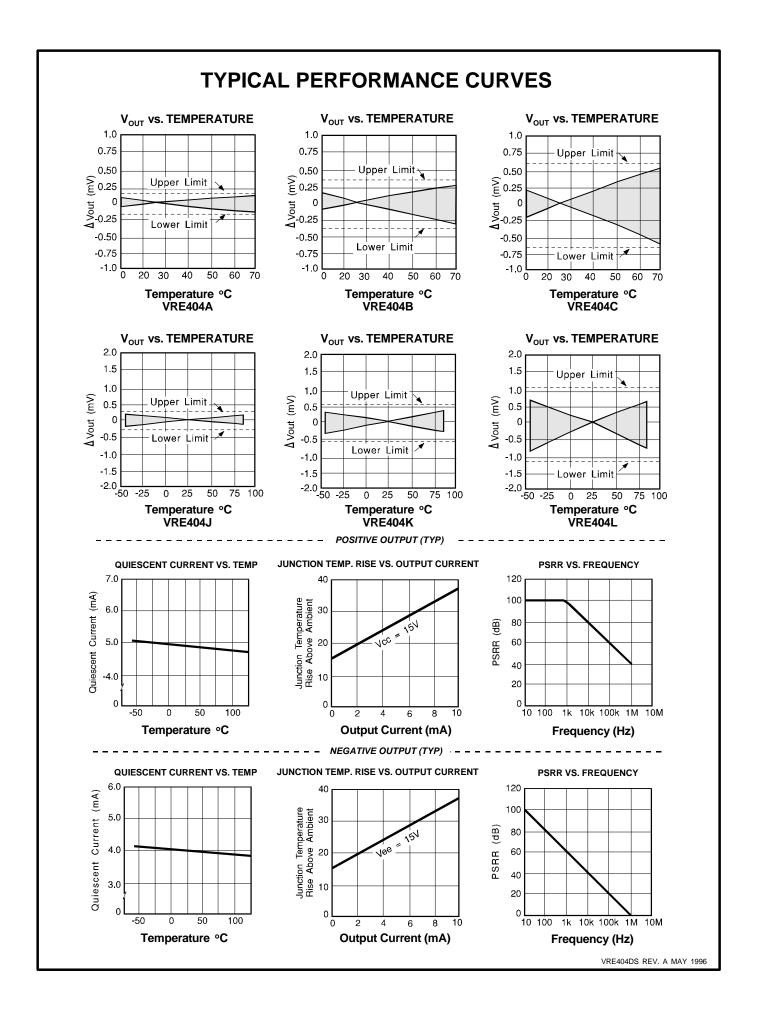
#### **SELECTION GUIDE**

Model	Initial Error mV	Temp. Coeff. ppm/°C	Temp. Range °C
VRE404A	0.45	0.6	0°C to +70°C
VRE404B	0.70	1.0	0°C to +70°C
VRE404C	0.90	2.0	0°C to +70°C
VRE404J	0.45	0.6	-40°C to +85°C
VRE404K	0.70	1.0	-40°C to +85°C
VRE404L	0.90	2.0	-40°C to +85°C

For package option add D for DIP or S for Surface Mount to end of model number.

ELECTRICAL SPECIFICATIONSVRE404Vps = $\pm 15V$ , T = 25°C, RL = 10K $\Omega$ unless otherwise noted.VRE404										
MODEL	A/J			B/K			C/L			
PARAMETER	MIN	ТҮР	МАХ	MIN	ТҮР	МАХ	MIN	ТҮР	МАХ	UNITS
ABSOLUTE RATINGS										
Power Supply Operating Temp. (A,B,C) Operating Temp. (J,K,L) Storage Temperature Short Circuit Protection	-40 -65	±15 Continuo	±22 +70 +85 +150 us	* * * *	*	* * * *	* * *	*	* * *	۷ ث ث
OUTPUT VOLTAGE										
VRE404		±4.5			*			*		V
OUTPUT VOLTAGE ERRORS										
Initial Error (1) Warmup Drift T <sub>min</sub> - T <sub>max</sub> <sup>(2)</sup> Tracking Error <sup>(3)</sup> Long-Term Stability Noise (.1-10Hz)		1 6 3	0.45 0.6 0.3		2 *	0.70 1.0 0.4		3	0.90 2.0 0.5	mV ppm ppm/ °C mV ppm/1000hrs μVpp
OUTPUT CURRENT										
Range	±10			*			*			mA
REGULATION										
Line Load		3 3	10		*	*		*	*	ppm/V ppm/mA
POWER SUPPLY CURRE	ENTS <sup>(4)</sup>									
+PS -PS		7 4	9 6		* *	* *		*	*	mA mA
NOTES: *Same as A/J 1. The specified values a 2. The temperature coeffi box method using the fo tc = $\frac{V_{max} - V_{mi}}{V_{nominal} \times (T_{minal} + T_{minal})}$	re withou cient (tc) Ilowing fo	) is deter ormula: x 10 <sup>6</sup>		y the	4. T	he spec	ified val	ues are	unloaded	

3. The tracking error is the deviation between the positive and negative output over the operating temp. range.



# **DISCUSSION OF PERFORMANCE**

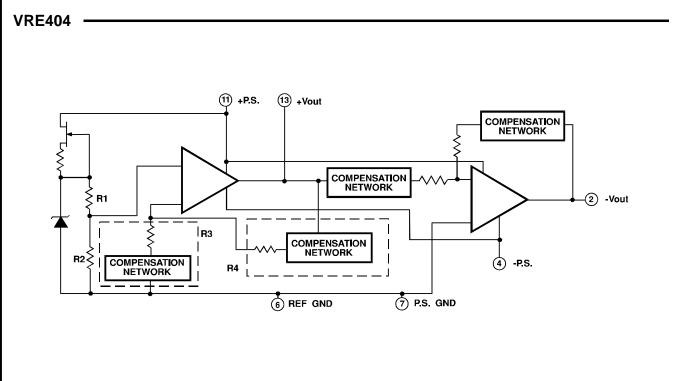
#### THEORY OF OPERATION

The following discussion refers to the schematic in figure 2 below. A FET current source is used to bias a 6.3V zener diode. The zener voltage is divided by the resistor network R1 and R2. This voltage is then applied to the noninverting input of the operational amplifier which amplifies the voltage to produce a 4.500V output. The gain is determined by the resistor networks R3 and R4: G=1 + R4/R3. The 6.3V zener diode is used because it is the most stable diode over time and temperature.

The current source provides a closely regulated zener current, which determines the slope of the references' voltage vs. temperature function. By trimming the zener current a lower drift over temperature can be achieved. But since the voltage vs. temperature function is nonlinear this compensation technique is not well suited for wide temperature ranges.

Thaler Corporation has developed a nonlinear compensation network of thermistors and resistors that is used in the VRE series voltage references. This proprietary network eliminates most of the nonlinearity in the voltage vs. temperature function. By adjusting the slope, Thaler Corporation produces a very stable voltage over wide temperature ranges. This network is less than 2% of the overall network resistance so it has a negligible effect on long term stability.

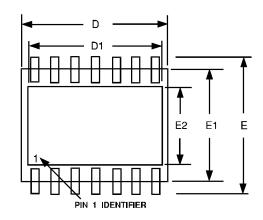
The VRE404 reference has it's ground brought out on two pins (pin 6 and 7) which are connected internally. This allows the user to achieve greater accuracy when using a socket. Voltage references have a voltage drop across their power supply ground pin due to quiescent current flowing through the contact resistance. If the contact resistance was constant with time and temperature, this voltage drop could be trimmed out. When the reference is plugged into a socket, this source of error can be as high as 20ppm. By connecting pin 7 to the power supply ground and pin 6 to a high impedance ground point in the measurement circuit, the error due to the contact resistance can be eliminated. If the unit is soldered into place, the contact resistance is sufficiently small that it does not effect performance.



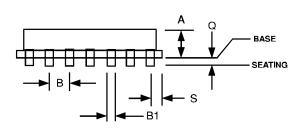
**FIGURE 2** 

## MECHANICAL

**FIGURE 3** 



	INC	HES	S MILLIMETER			INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX	DIM	MIN	MAX	MIN	MAX
А	.114	.136	2.90	3.45	E	.495	.526	12.5	13.3
В	.098	.103	2.48	2.62	E1	.390	.415	9.91	10.5
B1	.047	.056	1.19	1.42	E2	.265	.270	6.73	6.86
С	.103	.118	2.62	3.00	Р	.090	.110	2.29	2.79
C1	.009	.020	0.22	0.51	Q	.024	.035	0.61	.890
C2	.054	.062	1.37	1.57	s	.040	.060	1.02	1.52
D	.690	.715	17.5	18.1					
D1	.666	.680	16.9	17.2					



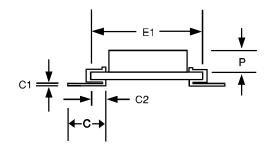


FIGURE 4

