

# ECONOMONITOR™ – 3-PIN SYSTEM SUPERVISOR WITH POWER SUPPLY MONITOR AND WATCHDOG

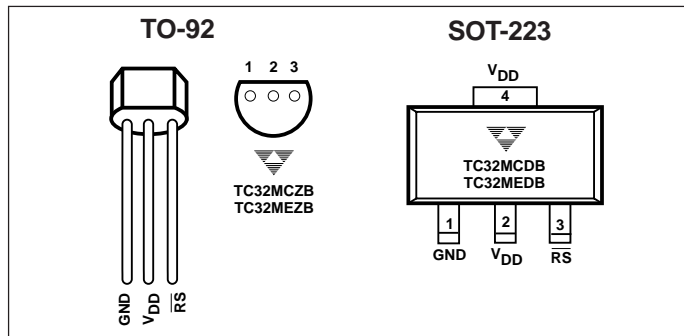
## FEATURES

- Incorporates the Functionality of the Industry Standard TC1232 (Processor Monitor, Watchdog and Manual Override RESET Controller) into a Small, Lower Cost Package
- Guards Against Unstable Processor Operation Resulting from Power "Brown-Out"
- Automatically Halts and Restarts an Out-of-Control Microprocessor
- Output can be Wire-ORed, or Hooked to Manual RESET Push-button Switch
- Space-Saving 3-Pin TO-92 or SOT-223 Package

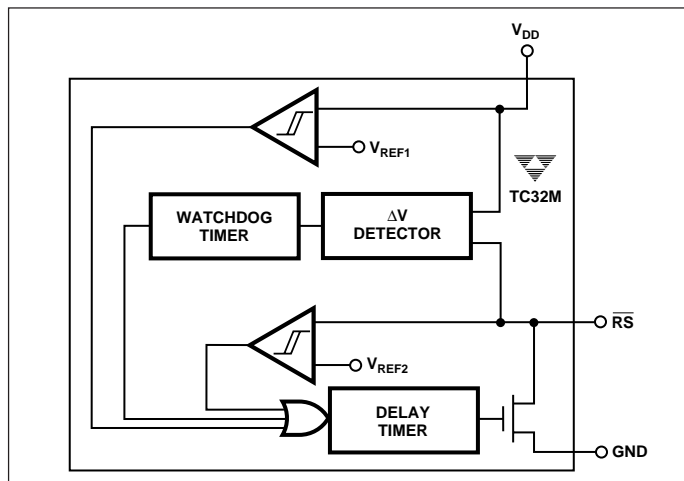
## APPLICATIONS

- All Microprocessor-Based Systems
- Battery Powered Computers and Controllers
- Automotive Systems
- Intelligent Instruments
- Critical Processor Monitoring
- Embedded Controllers

## PIN CONFIGURATIONS



## FUNCTIONAL BLOCK DIAGRAM



## GENERAL DESCRIPTION

The TC32M is a fully-integrated processor supervisor in a 3-pin package. It provides three important functions to safeguard processor sanity: precision power on/off RESET control, watchdog timer and external RESET override.

On power-up, the TC32M holds the processor in the reset state for a minimum of 500msec after  $V_{DD}$  is within tolerance to ensure a stable system start-up. Microprocessor sanity is monitored by the on-board watchdog circuit. The microprocessor must provide a high-to-low level shift (through an external resistor divider) on the RS pin of the TC32M. Should the processor fail to supply this signal within the specified timeout period (typically 700msec), an out-of-control processor is indicated and the TC32M issues a processor reset as a result.

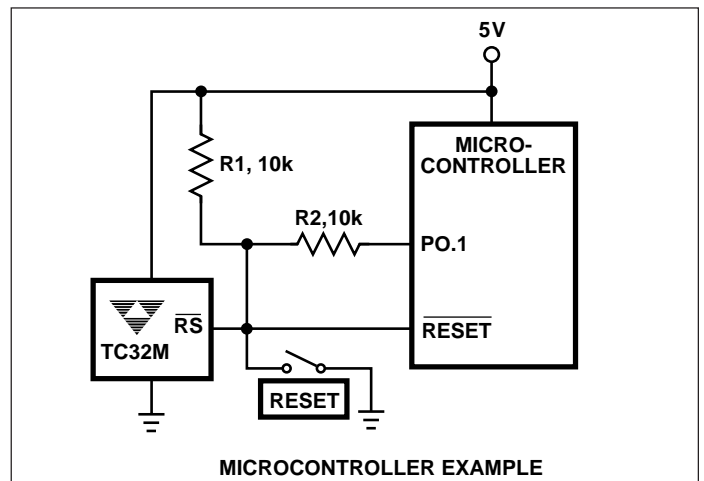
The output of the TC32M can be wire-ORed with a push-button switch (or electronic signal) to override the TC32M and unconditionally reset the processor. When connected to a push-button switch, the TC32M provides contact debounce.

The TC32M is packaged in a space-saving TO-92 or SOT-223 package. It provides all of the functionality of the industry standard TC1232 in a smaller, lower cost configuration.

## ORDERING INFORMATION

Part No.	Package	Temperature Range
TC32MCDB	SOT-223	0°C to +70°C
TC32MCZB	TO-92	0°C to +70°C
TC32MEDB	SOT-223	-40°C to +85°C
TC32MEZB	TO-92	-40°C to +85°C

## TYPICAL OPERATING CIRCUIT



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## TC32M

### ABSOLUTE MAXIMUM RATINGS \*

Supply Voltage .....	+6.0V
Input Voltage, Any Pin..... (GND – 0.3V) to (V <sub>DD</sub> + 0.3V)	
Operating Temperature Range	
TC32MC Package .....	0°C to +70°C
TC32ME Package .....	– 40°C to +85°C
Storage Temperature Range .....	– 65°C to +150°C
Lead Temperature (Soldering, 10 sec) .....	+300°C

\* Static-sensitive device. Unused devices must be stored in conductive material. Protect devices from static discharge and static fields. Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

### RECOMMENDED OPERATING CONDITIONS, DC: T<sub>A</sub> = – 40°C to +85°C, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V <sub>DD</sub>	Supply Voltage		4.5	—	5.5	V
V <sub>IH</sub>	$\overline{RS}$ Input HIGH Level for PB		2.0	—	—	V
V <sub>IL</sub>	$\overline{RS}$ Input LOW Level for PB		—	—	0.3	V

### ELECTRICAL CHARACTERISTICS, DC: V<sub>DD</sub> = 4.5V to 5.5V; T<sub>A</sub> = – 40°C to 85°C, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
I <sub>IL</sub>	$\overline{RS}$ Input Leakage		– 1	—	+1	μA
I <sub>OL</sub>	$\overline{RS}$ Output Current	V <sub>OL</sub> = 0.4V	2.0	10	—	mA
I <sub>CC</sub>	Operating Current	Note 1	—	50	200	μA
V <sub>STH</sub>	$\overline{RS}$ Strobe HIGH Level	Figure 1	(V <sub>DD</sub> – 0.5)	—	—	V
V <sub>STL</sub>	$\overline{RS}$ Strobe LOW Level	Figure 1	2.00	—	(V <sub>DD</sub> – 1.5)	V
V <sub>RST</sub>	RESET Threshold	V <sub>DD</sub> Falling (Note 2, Figure 3)	4.25	—	4.50	V

### ELECTRICAL CHARACTERISTICS, CAPACITANCE: T<sub>A</sub> = 25°C, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
C <sub>IN</sub>	Input Capacitance		—	—	5	pF
C <sub>OUT</sub>	Output Capacitance		—	—	7	pF

### ELECTRICAL CHARACTERISTICS, AC: T<sub>A</sub> = – 40°C to +85°C, V<sub>DD</sub> = 5V ±10%, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
t <sub>RST</sub>	RESET Active Time	Figure 2	500	700	900	msec
t <sub>ST</sub>	$\overline{RS}$ STROBE Pulse Width	Figure 1	500	—	—	nsec
t <sub>TD</sub>	Watchdog Timeout Period	Figure 1	500	700	900	msec
t <sub>RPD</sub>	V <sub>DD</sub> Detect to $\overline{RS}$ LOW	Figure 3	—	—	100	nsec

- NOTES:** 1. No output load.  
2. All voltages referenced to ground.

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TC32M

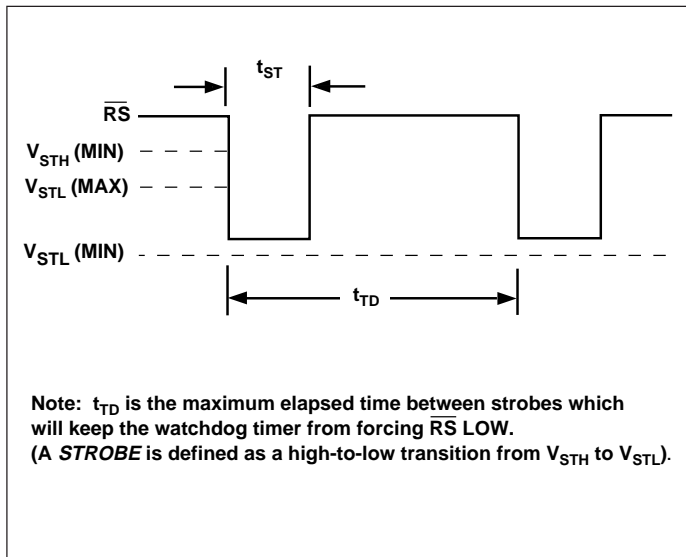


Figure 1. Watchdog Strobe

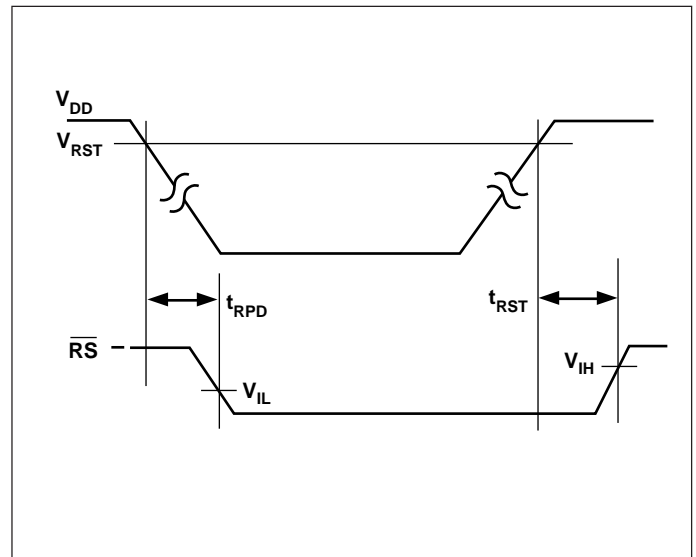


Figure 3. Power Up / Down Reset Timing

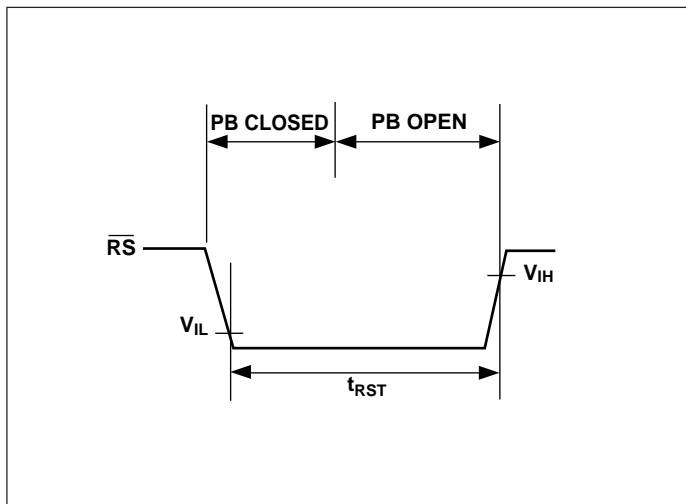


Figure 2.  $\overline{RS}$  Pulled Low by Push-button Reset

## PIN DESCRIPTION

Pin No.	Symbol	Description
1	GND	Ground.
2	$V_{DD}$	The +5V power supply input.
3	$\overline{RS}$	RESET/STROBE (Bidirectional). The open drain goes active if: <ol style="list-style-type: none"> <li>1. <math>V_{DD}</math> falls below 4.5V nominal.</li> <li>2. If pulled low by an external electronic signal or switch closure.</li> <li>3. If the watchdog is not strobed within the minimum watchdog timeout period.</li> <li>4. During power-up and power-down.</li> </ol> In the input mode, $\overline{RS}$ connects to a voltage level shift network (typically a resistor divider to $V_{DD}$ .) The watchdog timer is reset when processor causes a voltage level $\leq V_{STL}$ to be applied to $\overline{RS}$ .
4	$V_{DD}$	The +5V power supply input. (SOT-223 only).

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# ECONOMONITOR™ – 3-PIN SYSTEM SUPERVISOR WITH POWER SUPPLY MONITOR AND WATCHDOG

## TC32M

### DETAILED DESCRIPTION

The TC32M provides three important functions to safeguard stable processor operation: precision processor monitor, watchdog sanity timer and external override reset control.

#### Processor Monitor

The  $\overline{RS}$  pin is immediately driven low any time  $V_{DD}$  is below the nominal threshold voltage. As a result, this pin is LOW when power is initially applied, holding the processor in its reset state.  $\overline{RS}$  remains low for a minimum of 500msec after  $V_{DD}$  is within tolerance to allow the power supply and processor to stabilize.

#### Watchdog Timer

The processor drives the RS pin with an input/output (I/O) line in series with a resistor voltage divider to  $V_{DD}$ . Pulling the bottom resistor of this divider low results in an internal voltage change (*strobe*) sufficient to reset the watchdog timer, but above the  $V_{IL}$  input threshold of the processor  $\overline{RESET}$  pin. The processor must continuously apply strobes in this manner within a set period to verify proper software execution. A momentary reset (500msec minimum) is generated by the TC32M if a hardware or software failure keeps  $\overline{RS}$  from being strobed within the watchdog timeout period. This action typically initiates the processor's power-up routine. If the interruption persists, new reset pulses are generated each timeout period until  $\overline{RS}$  is strobed. This timeout period is typically 700msec.

The software routine that drives the  $\overline{RS}$  strobe must be in a section of the program that executes frequently enough

so the time between toggles is less than one watchdog timeout period. The strobe signal can be derived from microprocessor address, data and/or control signals. Typical circuit examples are shown in Figure 4.

#### Resistor Value Selection

The values of R1 and R2 must be chosen to ensure a valid low strobe level ( $V_{STL}$ ) on  $\overline{RS}$  when the processor I/O line is low. The use of 10k $\Omega$ ,  $\pm 5\%$  tolerance resistors are recommended. These values result in a nominal strobe level of 2.5 on  $\overline{RS}$  (min/max of 2.13V / 3.08V, assuming  $V_{DD} = 5.0V \pm 10\%$ ). Other resistor values can be used, so long as the additive tolerances of the power supply and resistor values result in a strobe that falls within  $V_{STH}$  and  $V_{STL}$  under all additive tolerance conditions.

#### External Override Reset Control

A built-in debounce circuit allows a push-button switch (PB) or other electronic signal to be wire-ORed to this pin as an external  $\overline{RESET}$  override control. The external  $\overline{RESET}$  is required to be an active low signal. Internally, this input is timed to provide a minimum  $\overline{RESET}$  pulse width of 500msec. Reference Figure 2.

#### Supply Monitor Noise Sensitivity

The TC32M is optimized for fast response to negative-going changes in  $V_{DD}$ . Systems with an inordinate amount of electrical noise on  $V_{DD}$  (such as systems using relays), may require a 0.01 $\mu F$  bypass capacitor to reduce detection sensitivity. This capacitor should be installed as close to the TC32M as possible to keep the capacitor lead length short.

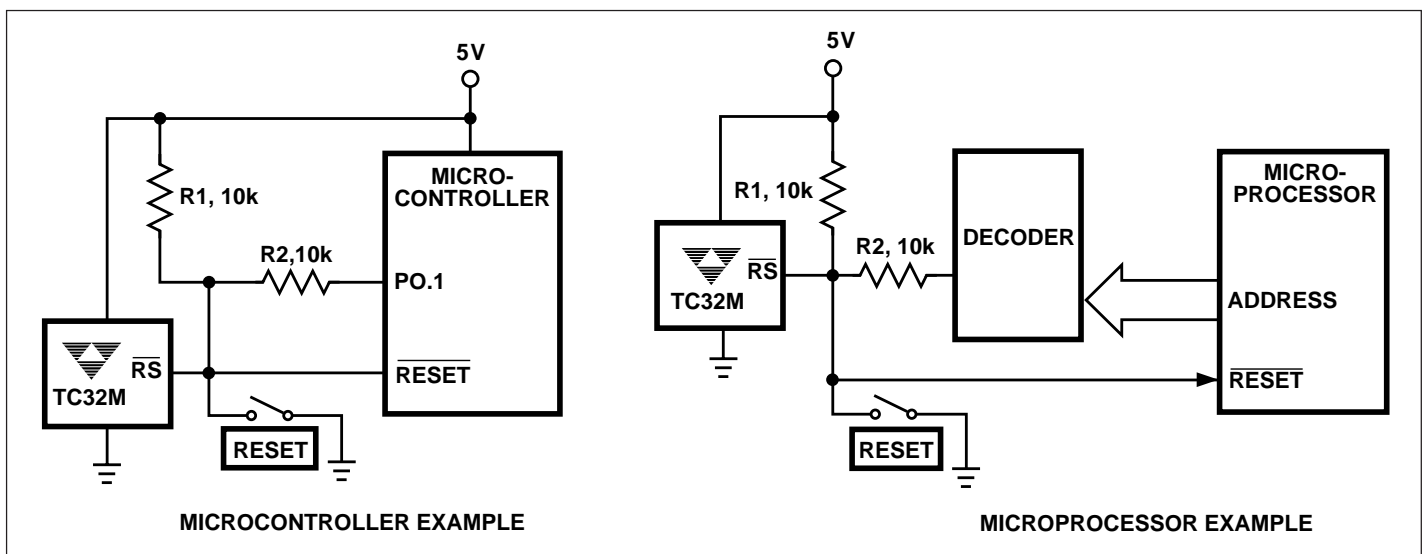


Figure 4. TC32M Hardware Connections (R1, R2 chosen to Meet  $V_{STH}$ ,  $V_{STL}$ )