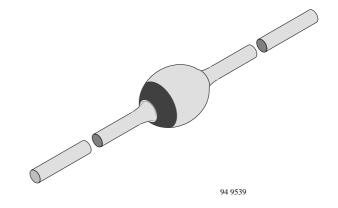
## **Silicon Mesa Rectifiers**

#### **Features**

- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- High surge current loading



## **Applications**

Rectifier, general purpose

## **Absolute Maximum Ratings**

 $T_j = 25^{\circ}C$ 

| Parameter                              | Test Conditions                        | Type  | Symbol                           | Value           | Unit              |
|--|--|-------|----------------------------------|-----------------|-------------------|
| Reverse voltage                        |  | BYX82 | $V_R = V_{RRM}$                  | 200             | V                 |
| =Repetitive peak reverse voltage       |  | BYX83 | $V_R = V_{RRM}$                  | 400             | V                 |
|  |  | BYX84 | $V_R = V_{RRM}$                  | 600             | V                 |
|  |  | BYX85 | $V_R = V_{RRM}$                  | 800             | V                 |
|  |  | BYX86 | $V_R = V_{RRM}$                  | 1000            | V                 |
| Peak forward surge current             | t <sub>p</sub> =10ms,<br>half sinewave |       | I <sub>FSM</sub>                 | 50              | Α                 |
| Repetitive peak forward current        |  |       | I <sub>FRM</sub>                 | 10              | Α                 |
| Average forward current                | T <sub>amb</sub> ≦45°C                 |       | I <sub>FAV</sub>                 | 2               | Α                 |
| i <sup>2</sup> *t-rating               |  |       | i <sup>2</sup> *t                | 8               | A <sup>2</sup> *s |
| Junction and storage temperature range |  |       | T <sub>j</sub> =T <sub>stg</sub> | <i>−</i> 65+175 | °C                |

### **Maximum Thermal Resistance**

 $T_i = 25^{\circ}C$ 

| Parameter        | Test Conditions                  | Symbol     | Value | Unit |
|------------------|----------------------------------|------------|-------|------|
| Junction ambient | I=10mm, T <sub>L</sub> =constant | $R_{thJA}$ | 45    | K/W  |
|                  | on PC board with spacing 25mm    | $R_{thJA}$ | 100   | K/W  |



#### **Electrical Characteristics**

 $T_i = 25^{\circ}C$ 

| Parameter               | Test Conditions   | Type | Symbol          | Min | Тур | Max | Unit |
|-------------------------|---|------|-----------------|-----|-----|-----|------|
| Forward voltage         | I <sub>F</sub> =1A  |      | $V_{F}$         |     | 0.9 | 1.0 | V    |
| Reverse current         | $V_R = V_{RRM}$   |      | I <sub>R</sub>  |     | 0.1 | 1   | μΑ   |
|                         | V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =100°C        |      | I <sub>R</sub>  |     | 10  | 25  | μΑ   |
| Diode capacitance       | V <sub>R</sub> =4V, f=0.47MHz                                   |      | $C_{D}$         |     | 20  |     | pF   |
| Reverse recovery time   | I <sub>F</sub> =0.5A, I <sub>R</sub> =1A, i <sub>R</sub> =0.25A |      | t <sub>rr</sub> |     | 2   | 4   | μs   |
| Reverse recovery charge | I <sub>F</sub> =I <sub>R</sub> =1A, di/dt=5A/μs                 |      | $Q_{rr}$        |     | 3   | 6   | μС   |

# **Characteristics** $(T_j = 25^{\circ}C \text{ unless otherwise specified})$

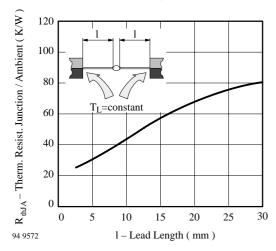


Figure 1. Max. Thermal Resistance vs. Lead Length

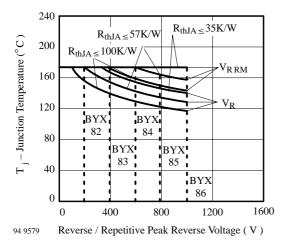


Figure 2. Junction Temperature vs. Reverse/Repetitive Peak Reverse Voltage

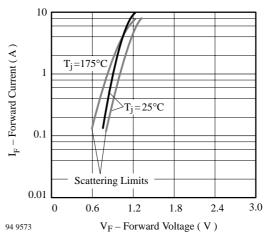


Figure 3. Forward Current vs. Forward Voltage

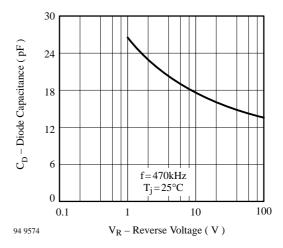


Figure 4. Typ. Diode Capacitance vs. Reverse Voltage



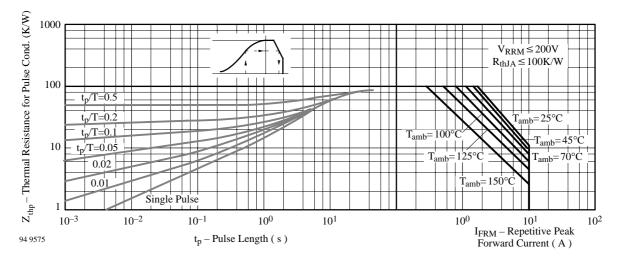


Figure 5. Thermal Response

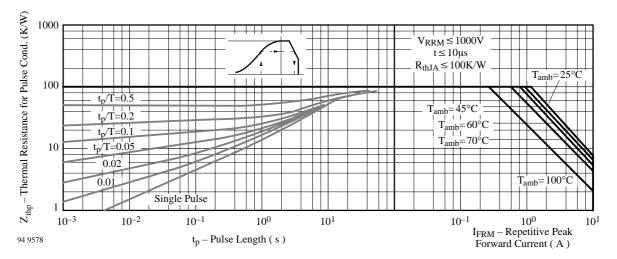


Figure 6. Thermal Response



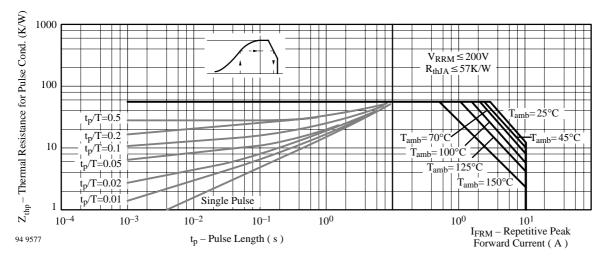


Figure 7. Thermal Response

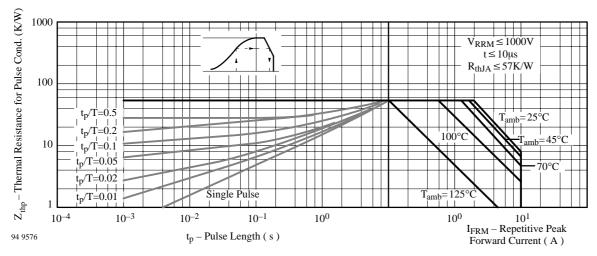
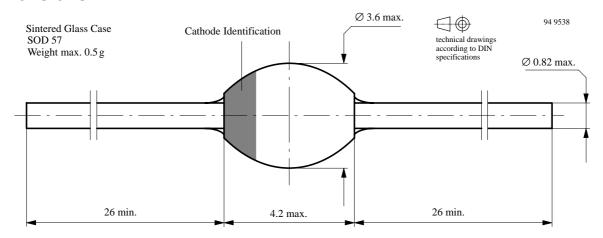


Figure 8. Thermal Response

#### **Dimensions in mm**





### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems

with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**Vishay Semiconductor GmbH** has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay-Telefunken products for any unintended or unauthorized application, the buyer shall indemnify Vishay-Telefunken against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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