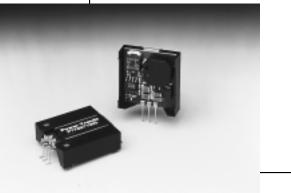
For assistance or to order; call (800) 531-5782

Series **PT78ST100**

1.5 AMP POSITIVE STEP-DOWN INTEGRATED SWITCHING REGULATOR

Revised 7/15/98

Application Notes



Very Small Footprint

- High Efficiency > 85%
- Self-Contained Inductor
- Internal Short-Circuit Protection
- **Over-Temperature Protection**
- Fast Transient Response
- Wide Input Range

Pin-Out Information

Vin

GND

Function

Pin

1

2

3

Vout

This is the new generation of PT78ST100 Series wide input range 3 terminal regulators. These ISRs have a maximum output current of 1.5 Amps and an output voltage that is laser trimmed to a variety of industry standard voltages.

These 78 series regulators have excellent line and load regulation with internal shortcircuit and over-temperature protection, and are offered in a variety of standard output voltages. These ISRs are very flexible and may be used in a wide variety of applications.

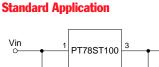
Package Suffix

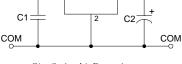
H = Horizontal

Mount

V = Vertical Mount

S = Surface Mount





C1 = Optional 1µF ceramic C2 = Required 100µF electrolytic

Specifications

Vout VERTICAL MOUNT HORIZONTAL MOUNT SUGGESTED BOARD LAYOUT Pkg Style 500

Output Voltage = 3.3 Volts = 3.6 Volts = 5.0 Volts = 5.1 Volts = 5.25 Volts = 6.0 Volts = 6.5 Volts = 7.0 Volts = 8.0 Volts = 9.0 Volts = 10.0 Volts 12 = 12.0 Volts 14 = 13.9 Volts 15 = 15.0 Volts

Ordering Information PT78ST1 XX || Y

Characteristics (T _a = 25°C unless noted)	Symbols	Conditions	PT78ST100 SERIES			
			Min	Тур	Max	Units
Output Current	Io	Over V _{in} range	0.1*	-	1.5	Α
Short Circuit Current	I _{sc}	$V_{in} = V_{in} \min$		3.5		Apk
Input Voltage Range	\mathbf{V}_{in}	$0.1 \le I_o \le 1.5A$ $V_o = 3.3V$ $V_o = 5V$ $V_o = 12V$	9 9 16	Ξ	26 38 38	V V V
Output Voltage Tolerance	ΔV_{o}	Over V_{in} range, $I_0=1.5A$ $T_a = 0^{\circ}C$ to +60°C	_	±1.0	±2.0	%V ₀
Line Regulation	Reg _{line}	Over V _{in} range	_	±0.2	±0.4	%Vo
Load Regulation	Reg _{load}	$0.1 \le I_o \le 1.5 A$	_	±0.1	±0.2	%Vo
V _o Ripple/Noise	V _n	V_{in} = 9V, I_o = 1.5A V_o = 5V V_{in} = 16V, I_o = 1.5A V_o = 12V	—	65 90	—	${}^{mV_{pp}}_{mV_{pp}}$
Transient Response (with 100μF output cap)	t _{tr}	50% load change V_0 over/undershoot	_	100 5	_	μSec %Vo
Efficiency	η			80 85 90		% % %
Switching Frequency	f_{o}	Over V _{in} range, I _o =1.5A	600	650	700	kHz
Absolute Maximum Operating Temperature Range	Та	-	-40	-	+85	°C
Recommended Operating Temperature Range	Та	Free Air Convection, (40-60LFM) At V_{in} = 24V, I_0 =1.0A	-40	_	+80**	°C
Thermal Resistance	θ_{ja}	Free Air Convection, (40-60LFM)	_	45	_	°C/W
Storage Temperature	T _s	_	-40	_	+125	°C
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3	_	500	_	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board	—	5	—	G's
Weight	_	_		6.5	_	grams

*ISR will operate down to no load with reduced specifications. **See Thermal Derating chart.

Note: The PT78ST100 Series requires a 100µF electrolytic or tantalum output capacitor for proper operation in all applications.

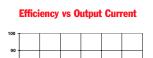
PT78ST100

CHARACTERISTIC DATA

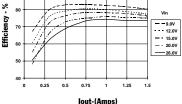
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e s





PT78ST133, 3.3 VDC (See Note 1)



Ripple vs Output Current

Ripple-(mV)

1.6 1.4 1.2

1

0.8

0.6

0.4

0.2

1.8 1.6

1.4 1.2

1 0.8

0.4

0.2

0.25 . 0.5 0.75

PD-(Watts)

0

13

17 Vin-(Volts)

Power Dissipation vs Output Current

lout-(Amps)

0.25 0.5 0.75 26.0V 20.0V 15.0V 12.0V 9.0V

1.25 1.5

(See Note 2)

25

26.0V 20.0V 15.0V 12.0V

lout-(Amps)

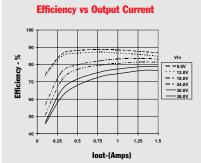
85°C

21

1.25 1.5

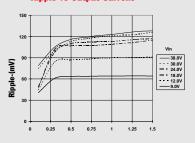
lout-(Amps)

Thermal Derating (T_a)



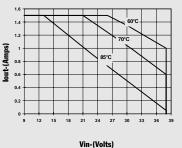
PT78ST105, 5.0 VDC (See Note 1)



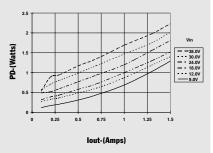


lout-(Amps)

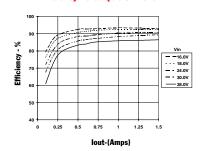




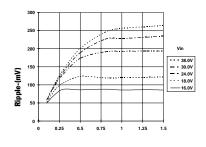
Power Dissipation vs Output Current







Ripple vs Output Current

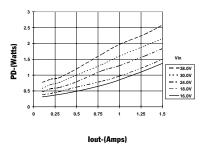


lout-(Amps)

Thermal Derating (T_a) (See Note 2) 70°C lout-(Amps) 0.8 0.6 0.4 0.2 16 22 19 25 31 34 37

Vin-(Volts)

Power Dissipation vs Output Current



Note 1: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note 2: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)

◄ ATA

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