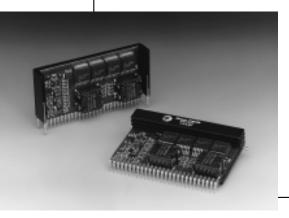
PT7772

Series

32 AMP HIGH-PERFORMANCE "SLEDGE HAMMER" PROGRAMMABLE ISR **Revised 7/15/98**

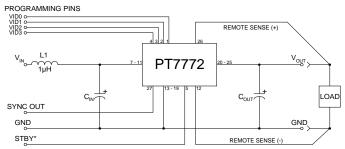


The PT7772 is a new series of high-performance, 32 Amp Integrated Switching Regulators (ISRs) housed in a 27-pin SIP package. The 32A capability allows easy integration of the latest high-speed, low-voltage µPs, ASICs, DSPs, and

bus drivers into existing 3.3V systems. The output voltage of the PT7772 can be easily programmed from 1.3V to 2.05V with a 4-bit input compatible with Intel's Pentium®Pro Processor. A differential remote sense is also provided which automatically compensates for any voltage drop from the ISR to the load.

2400μF of output capacitance are required for proper operation.

Standard Application



 $\begin{array}{ll} C_{in} &= Required\ 2400\mu F\ electrolytic \\ C_{out} &= Required\ 2400\mu F\ electrolytic \\ L1 &= Optional\ 1\mu H\ input\ choke \end{array}$

Pin-Out Information

	out illioillia
Pin	Function
1	VID0
2	VID1
3	VID2
4	VID3
5	STBY*- Stand-by
6	Do not connect
7	V_{in}
8	$ m V_{in}$
9	V_{in}
10	V_{in}
11	V_{in}
12	Remote Sense Gnd
13	GND
	·

Pin	Function
14	GND
15	GND
16	GND
17	GND
18	GND
19	GND
20	V_{out}
21	V_{out}
22	V_{out}
23	V_{out}
24	V_{out}
25	V_{out}
26	Remote Sense V_{out}
27	Sync Out

For STBY* pin; open = output enabled; ground = output disabled.

Specifications

Characteristics			PT7772 SI	PT7772 SERIES		
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	I_o	$T_a = +60$ °C, 200 LFM, pkg N $T_a = +25$ °C, natural convection	0.1 ⁽¹⁾ 0.1 ⁽¹⁾	=	32 26	A A
Input Voltage Range	V_{in}	$0.1A \le I_o \le 32A$	3.1(2)	_	3.6	V
Output Voltage Tolerance	ΔV_{o}	$V_{in} = +3.3V, I_o = 32A$ $0^{\circ}C \le T_a \le +55^{\circ}C$	Vo-0.03	_	Vo+0.03	V
Line Regulation	Reg _{line}	$3.1V \le V_{in} \le 3.6V, I_o = 32A$	_	±10	_	mV
Load Regulation	Regload	$V_{in} = +3.3V, 0.1 \le I_o \le 32A$	_	±10	_	mV
Vo Ripple/Noise pk-pk	V_n	$V_{in} = +3.3V$, $I_o = 32A$	_	50	_	mV
Transient Response with $C_{out} = 2400 \mu F$	$egin{array}{c} t_{ m tr} \ V_{ m os} \end{array}$	I _o step between 16A and 32A V _o over/undershoot	_	100 200	_	μSec mV
Efficiency	η	$V_{in} = +3.3V$, $I_o = 20A$, $V_o = 1.8V$	_	90	_	%
Switching Frequency	f_{o}	$3.1V \le V_{in} \le 3.6V$ $0.1A \le I_o \le 32A$	650	700	750	kHz
Absolute Maximum Operating Temperature Range	T_a		0	_	+85	°C
Recommended Operating Temperature Range	T_a	Forced Air Flow = 200 LFM Over $V_{in\ and} I_o$ Ranges	0	_	+65	°C
Storage Temperature	Ts	_	-40	_	+125	°C
Weight	_	Vertical/Horizontal		53/66	_	grams

⁽¹⁾ ISR-will operate down to no load with reduced specifications. Please note that this product is not short-circuit protected.

Output Capacitors: The PT7772 series requires a minimum output capacitance of 2400µF for proper operation. Do not use Oscon type capacitors. The maximum allowable output capacitance is 30,000µF.

Input Filter: An input filter is optional for most applications. The input inductor must be sized to handle 32ADC with a typical value of 1µH. The input capacitance must be rated for a minimum of 2.6Arms of ripple current. For transient or dynamic load applications, additional capacitance may be required.

⁽²⁾ The minimum input voltage is 3.1V or $V_{out}+1.2V$, whichever is greater.

SHEETS

T7772

Features

- +3.3V input
- 5-bit Programmable: 1.3V to 2.05V@32A
- High Efficiency
- Input Voltage Range: 3.1V to 3.6V
- Differential Remote Sense
- 27-pin SIP Package

Programming Information

VID3	VID2	VID1	VIDO	Vout
1	1	1	1	1.30V
1	1	1	0	1.35V
1	1	0	1	1.40V
1	1	0	0	1.45V
1	0	1	1	1.50V
1	0	1	0	1.55V
1	0	0	1	1.60V
1	0	0	0	1.65V
0	1	1	1	1.70V
0	1	1	0	1.75V
0	1	0	1	1.80V
0	1	0	0	1.85V
0	0	1	1	1.90V
0	0	1	0	1.95V
0	0	0	1	2.00V
0	0	0	0	2.05V

Logic 0 = Pin 12 potential (remote sense gnd) Logic 1 = Open circuit (no pull-up resistors) VID3 may not be changed while the unit is operating.

Ordering Information

 $PT7772 \square = 1.3 \text{ to } 2.05 \text{ Volts}$

For dimensions and PC board layout, see Package Style 1020 and 1030

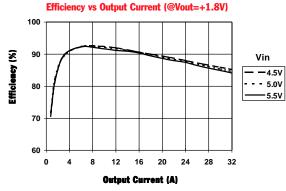
PT Series Suffix (PT1234X)

Case/Pin
Configuration

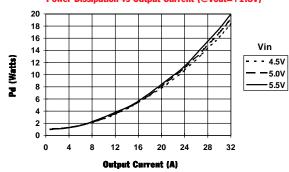
Co	nf	igur	ation

Vertical Through-Hole	N
Horizontal Through-Hole	A
Horizontal Surface Mount	C

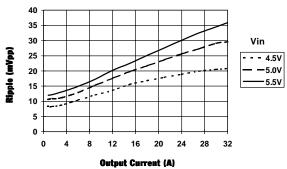
CHARACTERISTIC DATA



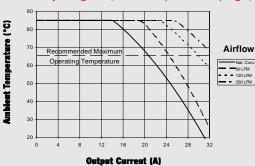
Power Dissipation vs Output Current (@Vout=+1.8V)



Output Ripple vs Output Current (@Vout=+1.8V)



Safe Operating Area (@Vin=+3.3V, Vout=+1.8V, Pkg N)



Note: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

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