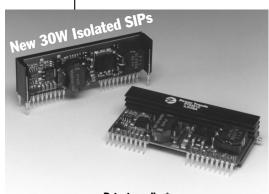
PT3340

30 WATT ISOLATED DC/DC CONVERTER

Application Notes Mechanical Outline Product Selector Guide



The PT3340 is a new series of highinput voltage, 30 Watt, isolated DC to DC converters housed in a unique vertical or horizontal 19-pin SIP package. The 18 to 60V input range allows easy integration into many distributed power applications which utilize 24V bus architectures.

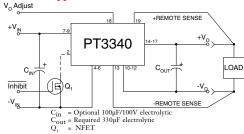
The PT3340 series is available with output voltages from 3.3V to 15V. The output voltage is adjustable from 90 to

110% of nominal with the addition of an external resistor. Other easy to use features include an inhibit function and a differential remote sense which automatically compensates for any voltage drop from the converter to the load. The PT3340 includes built in current limit, short circuit protection and overtemperature shutdown.

The PT3340 requires a 330µF output capacitor for proper operation.

Patent pending*

Standard Application



Pin-Out Information

Pin Function	Pin Function
1 Do Not Use	$10 - V_o$
2 Inhibit	11 -V _o
3 Do Not Use	12 -V _o
4 -V _{in}	13 -Remote Sense
5 -V _{in}	14 +V _o
6 -V _{in}	15 +V _o
7 +V _{in}	16 +V _o
8 +V _{in}	17 +V _o
9 +V _{in}	18 V _o Adjust
	19 +Remote Sense

Features

- 30W Output Power
- Input Voltage Range: 18V to 60V
- 1500 VDC Isolation
- V_o Inhibit
- V_oAdjust
- Differential Remote Sense
- Current Limit
- Short-Circuit Protection
- Over-Temperature Shutdown
- Undervoltage Lockout
- Flexibile SIP Package
- UL 1950, CSA 22.2 950 approval pending
- Meets EN60950

Preliminary Specifications

Characteristics			PT3340 SERIES			
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	I_{o}	$\begin{array}{c} Over \ V_{in} \ range & V_o = 3.3V \\ V_o = 5V \\ V_o = 12V \end{array}$	0.25 0.25 0.1		8.0 6.0 2.5	A A A
On/Off Standby Current	I _{in standby}	V_{in} = 24V, Pin 1 = - V_{in}	_	7	14	mA
Short Circuit Current	I_{sc}	$V_{\rm in}$ = 24V	_	I _{omax} x2	_	A
Input Voltage Range	V_{in}	Over Io Range	18.0	24.0	60.0	V
Output Voltage Tolerance	ΔV_{o}	Over V_{in} Range T_A = -40°C to +85°C	_	±1.0	_	$%V_{o}$
Line Regulation	Reg _{line}	Over V _{in} range @ max I _o		±0.5	_	$%V_{o}$
Load Regulation	Reg _{load}	10% to 100% of I_o max	_	±0.5	_	$%V_{o}$
V _o Ripple/Noise	V_n	V_{in} =24V, I_o = I_o max, V_o \geq 5V V_{in} =24V, I_o = I_o max, V_o <5V	=	1.0 50	_	$^{ m %V_o}_{ m mV_{pp}}$
Transient Response	t _{tr}	50% load change, 1A/μSec V _o over/undershoot, V _o ≥5V	=	100 3.0	Ξ	μSec %V _o
Efficiency	η	$\begin{array}{l} V_{in} = 24 V, I_o = 6 A, V_o = 3.3 V \\ V_{in} = 24 V, I_o = 6 A, V_o = 5 V \\ V_{in} = 24 V, I_o = 2.5 A, V_o = 12 V \end{array}$	=	80 82 86	=	% % %
Switching Frequency	f_{o}	$ \begin{array}{ccc} \mathrm{Over} \; V_{in} \; \mathrm{and} \; I_o & & V_o \!\!<\!\! 10 V \\ & & V_o \!\!\geq\!\! 10 V \end{array} $	600 400	750 500	900 600	kHz kHz
Recommended Operating Temperature Range	T_a	$V_{\rm in}$ = 48V @ max $I_{\rm o}$ Airflow = 200 LFM	-40*	_	+85	°C
Storage Temperature	T_s	_	-40		+125	°C
Mechanical Shock	_	Per Mil-Std-883D, method 2002.3, 1mS, half-sine, mounted to a fixture	_	500	_	G's
Mechanical Vibration	_	Per Mil-Std-883D, method 2007.2, 20-2000Hz, soldered in a PC board	_	10	_	G's
Weight	_	_	_	40	_	grams
Input/Output Isolation Capacitance Resistance		_	1500 10		=	VDC pF MΩ
Flammability	_	Materials meet UL 94V-0				
Inhibit (pin 2)	On** Off	Referenced to -Vin	2.5 0	_	15 0.8	VDC VDC

Ordering Information

 $PT3341\square = 3.3V/8A$ $PT3342\square = 5.0V/6A$ $PT3343 \square = 12.0 \text{V}/2.5 \text{A}$ $PT3344\square = 15.0V/2A$

PT Series Suffix (PT1234X)

Case/	D:
Case/	rm
CC	
Conn	guration

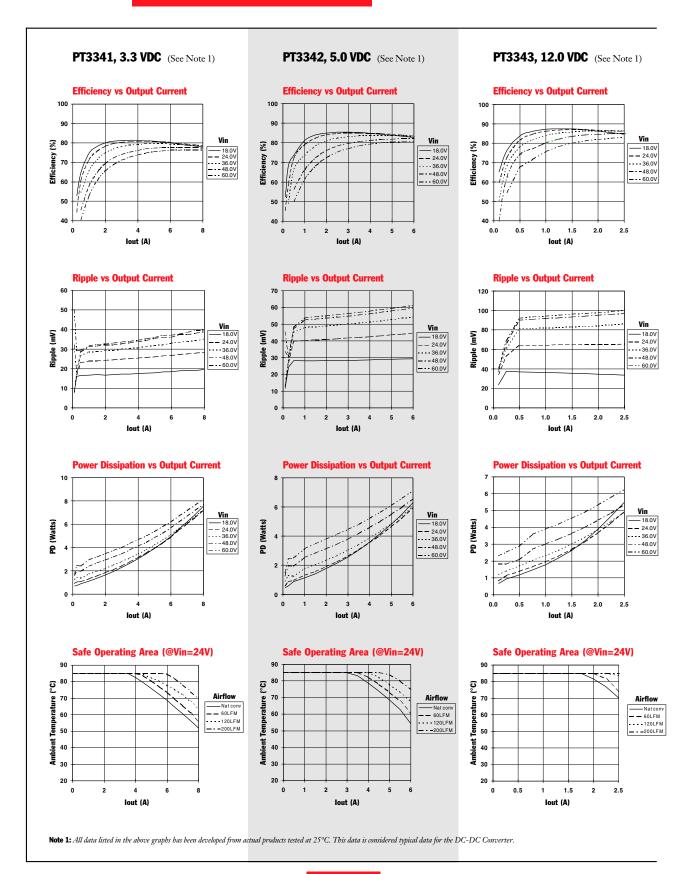
Vertical Through-Hole	N
Horizontal Through-Hole	Α
Horizontal Surface Mount	С

(For dimensions and PC board layout, see Package Styles 840 and 850.)

* **Note:** This product is the subject of one or more patents. Other patents pending.

^{*} At temperatures below 0°C, the PT3340 series requires output capacitors with temperature stable dielectrics such as tantalum or Oscon

^{**} If pin 2 is left open, the PT3340 will operate when input power is applied.



More Application Notes

Adjusting the Output Voltage of the PT3320 / PT3340 Series of Isolated DC-DC Converters

The factory pre-set output voltage of Power Trends' PT3320 and PT3340 series DC-DC converters may be adjusted within a nominal ±10% range. This is accomplished with the addition of a single external resistor. For the input voltage range specified in the data sheet, Table 1 gives the allowable adjustment range for each model as V_o (min) and V_o (max).

An increase in the output voltage is obtained by adding a resistor, R2 between pin 18 (V_o adjust), and pin 13 (-Remote Sense). See note 4.

Add a resistor (R1), between pin 18 (V_o adjust) and pin 19 (+Remote Sense).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor, (R1) or R2.

Notes:

- 1. Use only a single 1% resistor in either the (R1) or R2 location. Place the resistor as close to the ISR as possible.
- 2. Never connect capacitors to V_0 adjust. Any capacitance added to the Vo adjust control pin will affect the stability of

- 3. If the remote sense pins are not being used, the resistors (R1) and R2 can be connected to +Vout or -Vout respectively.
- 4. The adjusted output voltage V_a effectively sets the voltage across pins 13 and 19 (±Remote Sense). When using the remote sense pins, Vout (measured directly across pins 10-12, and 14-17) can be significantly higher than Va, and may exceed Vo (max). If Va is adjusted upward, the alternative is to increase the minimum input voltage by the same percentage as Vout exceeds Vo(max).

The values of (R1) [adjust down], and R2 [adjust up], can also be calculated using the following formulas.

$$R2 = \frac{R_o(V_o - V_r)}{(V_a - V_o)} - R_s \qquad k\Omega$$

Vo = Original output voltage Where

V_a = Adjusted output voltage

V_r = Reference voltage (Table 1)

R_o = Multiplier resistance (Table 1)

R_s = Series resistance (Table 1)

Table 1

DC-DC CONVERTER ADJUSTMENT RANGE AND FORMULA PARAMETERS							
Series Pt #							
24V Bus			PT3341	PT3342	PT3343	PT3344	
48V Bus	PT3325	PT3326	PT3321	PT3322	PT3323	PT3324	
V _O (nom)	2.0V	2.5	3.3	5.0	12.0	15.0	
Vo(min)	1.8V	2.25	2.95	4.5	10.8	13.5	
Vo(max)	2.2	2.75	3.65	5.5	13.2	16.5	
Vr	1.225	1.225	1.225	1.225	2.5	2.5	
R _o (kΩ)	80.6	33.2	33.2	18.2	14.3	11.0	
R _S (kΩ)	150.0	121.0	150.0	121.0	90.9	80.6	

Figure 1

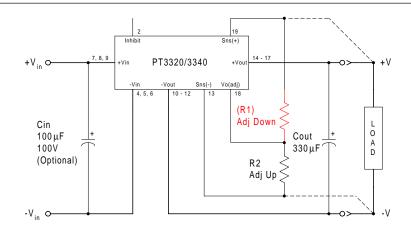


Table 2

eries Pt #								
24V Bus			PT3341		PT3342		PT3343	PT3344
48V Bus	PT3325	PT3326	PT3321		PT3322		PT3323	PT3324
Current	8Adc	8Adc	8Adc		6adc		2.5Adc	2.0Adc
V _o (nom)	2.0Vdc	2.5Vdc	3.3Vdc		5.0Vdc		12.0Vdc	15.0Vdc
V _a (req'd)				V _a (req'd		V _a (req'd)		
1.8	0.0kΩ			4.5	(246.0)kΩ	10.8	(285.0)kΩ	
1.85	(62.5)kΩ			4.55	(293.0)kΩ	11.0	(371.0)kΩ	
1.9	(194.0)kΩ			4.6	(352.0) k Ω	11.2	(500.0)kΩ	
1.95	(589.0)kΩ			4.65	(428.0)kΩ	11.4	(715.0)kΩ	
2.0				4.7	(529.0)kΩ	11.6	(1150.0)kΩ	
2.05	1100.0kΩ			4.75	(670.0) k Ω	11.8		
2.1	475.0kΩ			4.8	(882.0)kΩ	12.0		
2.15	266.0kΩ			4.85	(1230.0) k Ω	12.2	588.0kΩ	
2.2	162.0kΩ			4.9	(1940.0)kΩ	12.4	249.0kΩ	
2.25		(20.7)kΩ		4.95		12.6	136.0kΩ	
2.3		(64.7.0)kΩ		5.0		12.8	78.9kΩ	
2.35		(138.0)kΩ		5.05		13.0	45.0kΩ	
2.4		(285.0) k Ω		5.1	566.0kΩ	13.2	22.3kΩ	
2.45		(726.0) k Ω		5.15	337.0kΩ			
2.5				5.2	223.0kΩ	13.5		(323.0)kΩ
2.55		726.0kΩ		5.25	154.0kΩ	13.6		(355.0) k Ω
2.6		302.0 k Ω		5.3	108.0kΩ	13.8		(437.0) k Ω
2.65		161.0 k Ω		5.35	75.3kΩ	14.0		(522.0) k Ω
2.7		90.6kΩ		5.4	50.8kΩ	14.2		(724.0) k Ω
2.75		48.3kΩ		5.45	31.7kΩ	14.4		(1010.0) k Ω
2.95			(127.0) k Ω	5.5	16.4kΩ	14.6		(1580.0) k Ω
3.0			(183.0) k Ω			14.8		
3.05			(261.0) k Ω			15.0		
3.1			(377.0) k Ω			15.2		607.0kΩ
3.15			(572.0) k Ω			15.4		263.0kΩ
3.2			(961.0)kΩ			15.6		149.0kΩ
3.25			(2130.0) k Ω			15.8		91.3kΩ
3.3						16.0		56.9kΩ
3.35			1230.0kΩ			16.5		11.1kΩ
3.4	·		539.0kΩ					
3.45			309.0kΩ					
3.5			194.0kΩ					
3.55			126.0kΩ					
3.6			79.6kΩ					
3.65			46.8kΩ					

R1 = (Red)

R2 = Black

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