20

19

18

17

16

15

14

13

12

11

**PWP PACKAGE** 

(TOP VIEW)

GND 🗖

GND 🗖

RFIN

GND 🗖

NC 🗆

VPC 🗖

GND 🗖

NC I

V<sub>BB</sub>

GND 🗖

10

2

3

4

5

6

7

8

9

10

NC - No internal connection

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🗖 GND

□ GND
□ GND

T TXEN

🗖 GND

T RFOUT

- Operates from 3.6-V and 4.8-V Power Supplies for AMPS/NADC and GSM Applications Respectively
- Unconditionally Stable
- Wide UHF Frequency Range 800 MHz to 1000 MHz
- 21 dBm and 23 dBm Typical Output Power in AMPS/NADC and GSM Applications Respectively
- Linear Ramp Control
- Transmit Enable/Disable Control
- Advanced BiCMOS Processing Technology for Low-Power Consumption, High Efficiency, and Highly Linear Operation
- Minimum of External Components Required for Operation
- Surface-Mount Thermally Enhanced Package for Extremely Small Circuit Footprint

## description

The TRF8010 is an RF transmit driver amplifier for 900-MHz digital, analog, and dual-mode communication applications. It consists of a two-stage amplifier and a linear ramp controller for burst control in TDMA (time division multiple access) applications. Very few external components are required for operation.

The TRF8010 amplifies the RF signal from the preceding modulator and upconverter stages in an RF section of a transmitter to a level that is sufficient to drive a final RF power output device. The output impedance of RFOUT is approximately 50  $\Omega$ . But, since RFOUT is connected to an open-collector output device, minimal external matching is required.

The device is enabled when the TXEN input is held high. A power control signal applied to the VPC input can ramp the RF output power up or down to meet ramp and spurious emission specifications in TDMA systems. The power control signal causes a linear change in output power as the voltage applied to VPC varies between 0 V and 3 V. With the RF input power applied to RFIN at 0 dBm and TXEN high, adjusting VPC from 0 V to 3 V increases the output power from a typical value of -54 dBm at VPC = 0 V to the output power appropriate for the application:

- 21 dBm typical for AMPS/NADC (Advanced Mobile Phone Service/North American Digital Cellular) operation
- 23 dBm typical for GSM (Global System for Mobile Communications) operation

Forward isolation with the RF input power applied to RFIN at 0 dBm, VPC = 0 V, and TXEN high is typically greater than 50 dB.



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

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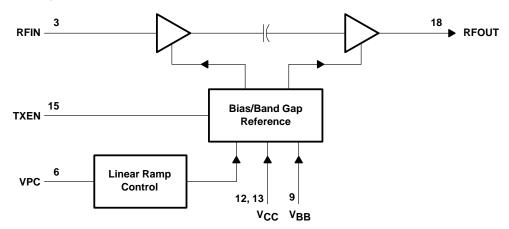
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#### description (continued)

The TRF8010 is available in a small, surface-mount, thermally enhanced TSSOP 20-pin PWP (PowerPAD<sup>TM</sup>) package and is characterized for operation from  $-40^{\circ}$ C to  $85^{\circ}$ C. The PWP package has a solderable pad that can improve the package thermal performance by bonding the pad to an external thermal plane. The pad also acts as a low-inductance electrical path to ground and, for the TRF8010, must be electrically connected to the PCB ground plane as a continuation of the regular package terminals that are designated GND.

## functional block diagram



## **Terminal Functions**

	TERMINAL		DECODIDITION
NAME	NO.	I/O	DESCRIPTION
GND	1, 2, 4, 7, 10, 11, 14, 16, 17, 19, 20		Analog ground for all internal analog circuits. All signals are referenced to the ground terminals.
NC	5, 8		No connection. It is recommended that all NC terminals be connected to ground.
RFIN	3	I	RF input. RFIN accepts signals between 800 MHz and 1000 MHz.
RFOUT	18	0	RF output. RFOUT is an open-collector output and requires a decoupled connection to $V_{CC}$ for operation.
TXEN	15	Ι	Transmit enable input (digital). When TXEN is high, the output device is enabled.
V <sub>BB</sub>	9		Control section supply voltage.
Vcc	12, 13		First stage bias.
VPC	6	Ι	Voltage power control. VPC is a signal between 0 V and 3 V that adjusts the output power from a typical value of $-54$ dBm to the maximum output power appropriate for the application.

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#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CC</sub> (see Note 1) Input voltage range at TXEN, VPC Input power at RFIN Thermal resistance, junction to case, $R_{\theta JC}$ (see Note 2) Thermal resistance, junction to ambient, $R_{\theta JA}$ (see Note 3) Continuous total power dissipation at $T_A = 25^{\circ}C$ Operating junction temperature, $T_J$ Junction temperature, $T_J$ max Operating free-air temperature range, $T_A$	0.6 V to 5.6 V 
Operating free-air temperature range, T <sub>A</sub> Storage temperature range, T <sub>stg</sub>	

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. Voltage values are with respect to GND.

- 2. No air flow and with infinite heatsink
- 3. With the thermal pad of the device soldered to a 1-ounce copper (Cu) ground plane on an FR4 board with no air flow

### recommended operating conditions

	MIN	NOM MAX	UNIT
Supply voltage, V <sub>CC</sub> (see Note 1)	3	5	V
High-level input voltage at TXEN, VIH	V <sub>CC</sub> -0.8		V
Low-level input voltage at TXEN, VIL		0.8	V
Operating free-air temperature, T <sub>A</sub>	-40	85	°C

NOTE 1: Voltage values are with respect to GND.

### electrical characteristics over full range of operating conditions

#### supply current, V<sub>CC</sub> = 3.6 V

	PAF	TEST CONDITIONS	MIN TYP <sup>‡</sup> MAX	UNIT	
	Operating at maximum power out	TXEN high, VPC = 3 V	163	mA	
ICC Supply current from VCC		Operating at minimum power out	TXEN high, VPC = 0 V	7	mA
					•

<sup>‡</sup>Typical values are at  $T_A = 25^{\circ}C$ .

## supply current, $V_{CC}$ = 4.8 V

PA	TEST CONDITIONS	MIN	TYP‡	MAX	UNIT	
	Operating at maximum power out	TXEN high, VPC = 3 V		155	210	mA
I <sub>CC</sub> Supply current from V <sub>CC</sub>	Operating at minimum power out	TXEN high, VPC = 0 V		7		mA
	Power down	TXEN low, VPC = 0 V			0.05	mA

<sup>‡</sup>Typical values are at  $T_A = 25^{\circ}C$ .



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## AMPS/NADC operation, V<sub>CC</sub> = 3.6 V, TXEN high, VPC = 3 V, T<sub>A</sub> = 25°C (unless otherwise noted)

		PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Operating freque	ency range		824		849	MHz
De			$P_{I} = 0 \text{ dBm}$		21		dBm
PO	Output power		$P_I = 0 dBm  VPC = 0 V$		-58		иып
	Gain (small sign	al)	$P_{I} = -20 \text{ dBm}$		27		dB
	Power added efficiency (PAE) Input return loss (internally matched)		$P_{I} = 0 \text{ dBm}$	28%			
			$P_{I} = -20 \text{ dBm}$	11	11		dB
	Output return los	ss (externally matched, small signal)	$P_{I} = -20 \text{ dBm}$		11		dB
	Noise power in 3	30 kHz bandwidth	45 MHz offset at P <sub>I</sub> = 0 dBm		-97		dBm
	Llarmaniaa	2f <sub>0</sub>			-20		15.
	Harmonics 3f <sub>0</sub>		Pl = 0 dBm		-50		dBc

# GSM operation<sub>CC</sub> $\neq$ 4.8 V, TXEN high, VPC = 3 V, T<sub>A</sub> = 25°C (unless otherwise noted)

	PARAMETEI	२	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Extended GSM operating frequency	range		870		925	MHz
De	Output power		Pl = 0 dBm	21.5	23	24.5	dBm
PO			$P_I = 0 dBm$ VPC = 0 V		-54		авт
	Gain (small signal)		$P_I = -20 \text{ dBm}$		28		dB
	Power added efficiency (PAE)		$P_{I} = 0 dBm$		29%		
	Input return loss (internally matched)		$P_I = -20 \text{ dBm}$		11		dB
	Output return loss (externally matched	ed, small signal)	P <sub>I</sub> = -20 dBm		11		dB
	Harmonics	2f <sub>0</sub>	P <sub>l</sub> = 0 dBm		-28	-22	dBc
	Harmonics	3f <sub>0</sub>			-40	-35	uвс
	Noise newer in 20 kHz handwidth	20 MHz above f <sub>0</sub>	P <sub>1</sub> = 0 dBm		-95		dBm
	Noise power in 30 kHz bandwidth	Noise power in 30 kHz bandwidth 10 MHz above f <sub>0</sub>			-96		uBIII

## stability, AMPS/NADC and GSM operation

PARAMETER	TEST CONDITIONS	MIN	ΤΥΡ	MAX	UNIT
Stability	Output VSWTR< 6:1 all phases, V <sub>CC</sub> < 5.6 V, P <sub>I</sub> = 0 dBm, P <sub>O</sub> ≤ 22 dBm, Output frequency band: 200 MHz - 1200 MHz		‡		

<sup>†</sup>VSWR = voltage standing wave ratio

<sup>‡</sup> No parasitic oscillations (all spurious < -70 dBc)

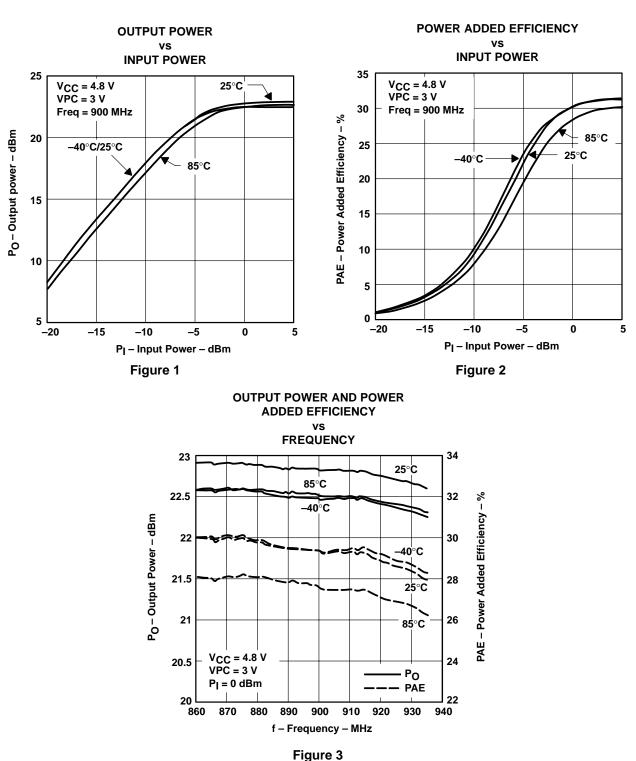
#### switching characteristics

# AMPS/NADC and GSM operation, V<sub>CC</sub> = 3.6 V or 4.8 V, T<sub>A</sub> = 25°C

PARAMETER		PARAMETER TEST CONDITIONS		TYP	MAX	UNIT
ton	Switching time, RF output OFF to ON	TXEN = high, VPC stepped from 0 V to 3 V		1		μs
toff	Switching time, RF output ON to OFF	TXEN = high, VPC stepped from 3 V to 0 V		1		μs



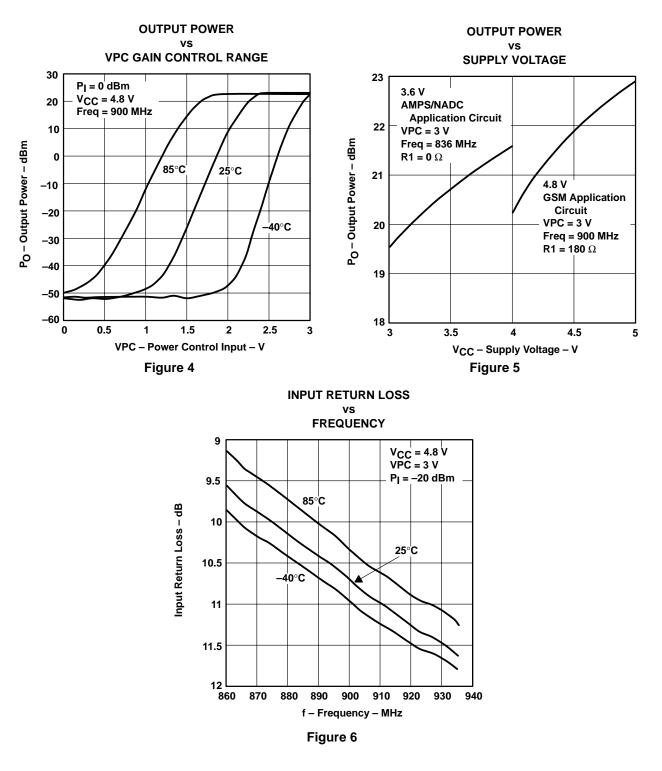
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**TYPICAL CHARACTERISTICS** 



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**TYPICAL CHARACTERISTICS** 



## **APPLICATION INFORMATION**

A typical application example for AMPS/NADC cellular telephone systems is shown in Figure 7.

In all cases, a capacitor must be connected from the positive power supply to ground, as close as possible to the IC terminals for power supply bypassing. A dc-blocking capacitor is also required on the RF output. A list of components and their functions is given in Table 1.

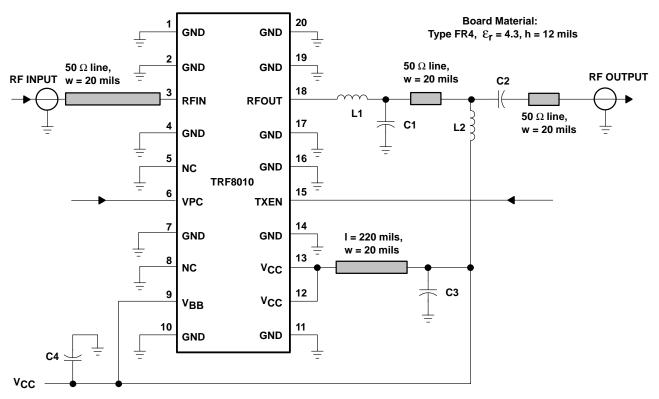


Figure 7. Typical AMPS/NADC (	Cellular Telephone Application
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COMPONENT DESIGNATION	TYPICAL VALUE (AMPS/NADC)	FUNCTION
C1	3.3 pF	Output impedance matching capacitor
C2	100 pF	DC-blocking capacitor for RF output
C3	100 pF	Matching capacitor
C4	1000 pF	Power supply decoupling capacitor
L1	5.7 nH	Output impedance matching inductor
L2	100 nH	DC bias/RF choke



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## **APPLICATION INFORMATION**

A typical application example for GSM cellular telephone systems is shown in Figure 8.

In all cases, a capacitor must be connected from the positive power supply to ground, as close as possible to the IC terminals for power supply bypassing. A dc-blocking capacitor is also required on the RF output. A list of components and their functions is given in Table 2.

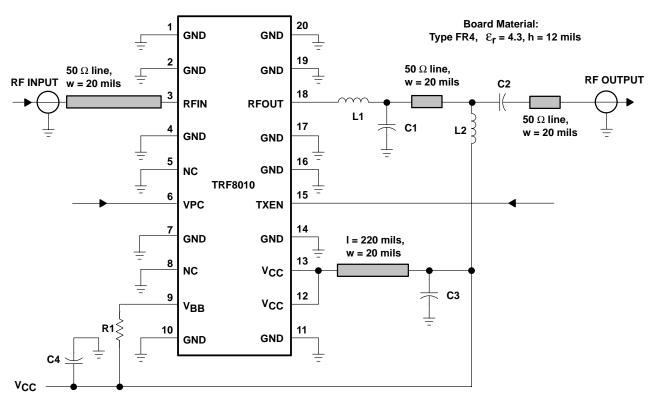


Figure 8.	Typical GSM	A Cellular Te	elephone A	oplication
i igai o oi	Typical Con			phoation

COMPONENT DESIGNATION	TYPICAL VALUE (GSM)	FUNCTION	
C1	3.3 pF	Output impedance matching capacitor	
C2	100 pF	DC-blocking capacitor for RF output	
C3	100 pF	Matching capacitor	
C4	1000 pF	Power supply decoupling capacitor	
L1	6.8 nH	Output impedance matching inductor	
L2	100 nH	DC bias/RF choke	
R1	180 Ω	Bias supply resistor	

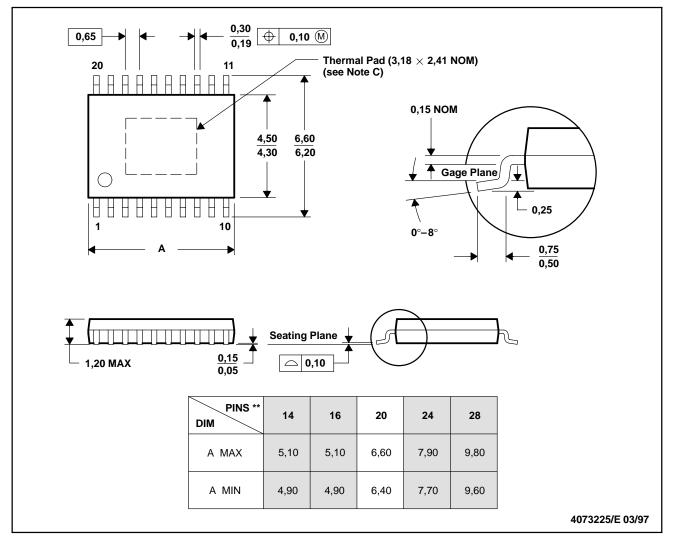
#### Table 2. External Component Selection (GSM)



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#### MECHANICAL DATA

## PowerPAD<sup>™</sup> PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

PWP (R-PDSO-G\*\*)

B. This drawing is subject to change without notice.

C. The package thermal performance may be enhanced by bonding the thermal pad to an external thermal plane. This solderable pad is electrically and thermally connected to the backside of the die and leads 1, 10, 11, and 20.

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