SDAS074B - APRIL 1982 - REVISED JANUARY 1995

- 'AS1004A Offer High Capacitive-Drive Capability
- Driver Version of 'ALS04B and 'AS04
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

description

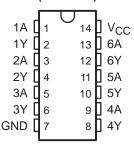
These devices contain six independent inverting drivers. They perform the Boolean function $Y = \overline{A}$.

The SN54AS1004A is characterized for operation over the full military temperature range of -55° C to 125°C. The SN74ALS1004 and SN74AS1004A are characterized for operation from 0°C to 70°C.

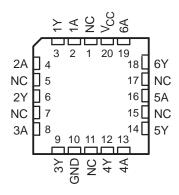
FUNCTION TABLE (each inverter)

INPUT A	OUTPUT Y
Н	L
L	Н

SN54AS1004A . . . J PACKAGE SN74ALS1004, SN74AS1004A . . . D OR N PACKAGE (TOP VIEW)

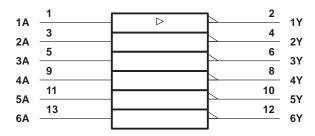


SN54AS1004A . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

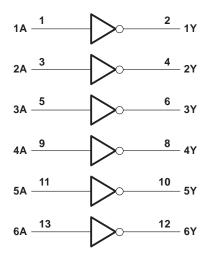
logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

logic diagram (positive logic)



SN54AS1004A, SN74ALS1004, SN74AS1004A HEX INVERTING DRIVERS

SDAS074B - APRIL 1982 - REVISED JANUARY 1995

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

recommended operating conditions

		SN74ALS1004			UNIT
		MIN	NOM	MAX	UNIT
Vсс	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
IOH	High-level output current			-15	mA
IOL	Low-level output current			24	mA
TA	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS			SN74ALS1004		
PARAMETER				TYP‡	MAX	UNIT
VIK	V _{CC} = 4.5 V,	I _I = -18 mA			-1.5	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2)		
Vон	V _{CC} = 4.5 V	IOH = -3 mA	2.4	3.2		V
		I _{OH} = -15 mA	2			
V	V _{CC} = 4.5 V	I _{OL} = 12 mA		0.25	0.4	V
V _{OL}		I _{OL} = 24 mA		0.35	0.5	
l _l	V _{CC} = 5.5 V,	V _I = 7 V			0.1	mA
lН	V _{CC} = 5.5 V,	V _I = 2.7 V			20	μΑ
I _{IL}	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1	mA
ΙΟ§	V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	mA
ICCH	V _{CC} = 5.5 V,	V _I = 0		0.84	3	mA
^I CCL	V _{CC} = 5.5 V,	V _I = 4.5 V		7	12	mA

 $[\]ddagger$ All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	SN74ALS1004 MIN MAX 1 7		UNIT
^t PLH	Δ.	V	1	7	20
t _{PHL}	А	1	1	6	ns

 $[\]P$ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[§] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

SDAS074B - APRIL 1982 - REVISED JANUARY 1995

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	
Input voltage, V _I	
Operating free-air temperature range, T _A : SN54AS1004A	55°C to 125°C
SN74AS1004A	0°C to 70°C
Storage temperature range	65°C to 150°C

recommended operating conditions‡

		SN54AS1004A		SN7	UNIT			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
ІОН	High-level output current			-40			-48	mA
IOL	Low-level output current			40			48	mA
TA	Operating free-air temperature	-55		125	0		70	°C

[‡] These high sink- or source-current devices are not recommended for use above 40 MHz.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN5	SN54AS1004A			SN74AS1004A		
PARAMETER			MIN	TYP§	MAX	MIN	TYP§	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	I _I = -18 mA			-1.2			-1.2	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2			V _{CC} -2			
Voн		$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V
VOH	V _{CC} = 4.5 V	$I_{OH} = -40 \text{ mA}$	2						\ \ \
		$I_{OH} = -48 \text{ mA}$				2			
VOL	V _{CC} = 4.5 V	$I_{OL} = 40 \text{ mA}$		0.25	0.5				V
VOL.		$I_{OL} = 48 \text{ mA}$					0.35	0.5	V
lį	$V_{CC} = 5.5 V$,	V _I = 7 V			0.1			0.1	mA
lіН	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ
Ι _Ι L	$V_{CC} = 5.5 V,$	V _I = 0.4 V			-0.5			-0.5	mA
ΙΟ [¶]	$V_{CC} = 5.5 V,$	V _O = 2.25 V	-50		-200	-50		-200	mA
Іссн	$V_{CC} = 5.5 V,$	V _I = 0		3.5	5		3.5	5	mA
ICCL	$V_{CC} = 5.5 V,$	V _I = 4.5 V		16	27		16	27	mA

[§] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

SN54AS1004A, SN74ALS1004, SN74AS1004A HEX INVERTING DRIVERS

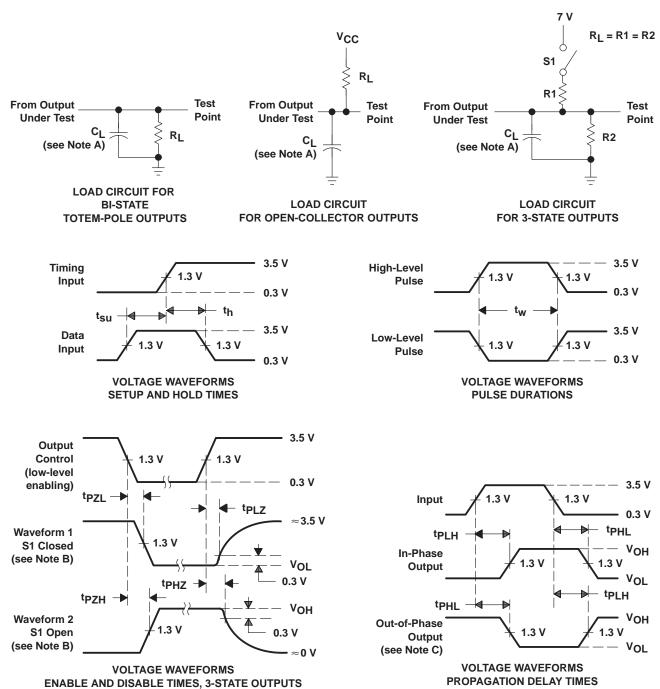
SDAS074B - APRIL 1982 - REVISED JANUARY 1995

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _C (C _L R _L T _A		UNIT		
			MIN	MAX	MIN	MAX	
t _{PLH}	۸	Y	1	5	1	4	ns
^t PHL	A		1	5	1	4	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: $PRR \le 1$ MHz, $t_f = t_f = 2$ ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated