DECEMBER 1983-REVISED MARCH 1988

- Package Options Include Plastic and Ceramic DIPs and Ceramic Flat Packages
- Dependable Texas Instruments Quality and Reliability

description

These devices contain dual 4-input positive NOR gates with strobe. They perform the Boolean function:

$$Y = \overline{G(A+B+C+D)}$$
(with 1X and 1X of '23 left open).

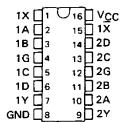
The SN5423 and the SN5425 are characterized for operation over the full military temperature range of $-55\,^{\circ}\text{C}$ to 125 °C. The SN7423 and the SN7425 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE

	11	NPU1		OUTPUT	
Α	В	С	D	G	Y
Н	х	×	×	н	L
х	Н	X	X	н	L
X	×	Н	X	н	L
Х	×	х	Н	Н	L
L	L	L	L	x	н
×	х	х	Х	L	н

Expander inputs are open, H = high level, L = low level, X = irrelevant

SN5423 . . . J OR W PACKAGE SN7423 . . . N PACKAGE (TOP VIEW)

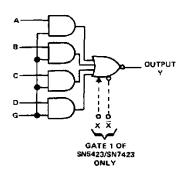


SN5425 . . . J OR W PACKAGE SN7425 . . . N PACKAGE (TOP VIEW)

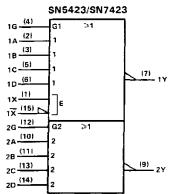
1А Д́т	
18 □2	13 D 2D



logic diagram



logic symbols†



[†]These symbols are in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12. Pin numbers are for J, N, or W packages.

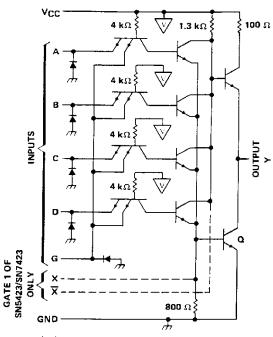
SN5425/SN7425 (1) (2) (6) (4) (5) 10-(11)2G -G2 ≥1 (9) ZA-(8)__2Y (101 28 (12) 2C 2D (13)

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS VI

SN5423, SN5425, SN7423, SNSN7425 DUAL 4-INPUT NOR GATES WITH STROBE

schematic (each gate)



NOTES: A. Component values shown are nominal.

- B. Both expander inputs are used simultaneously for expanding.
- C. If expander is not used leave X and X open.
- D. A total of four expander gates can be connected to the expander inputs.
- VCC bus

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage VCC (see Note 1)		
Input voltage (see Note 1)		
Operating free-air temperature range:	SN5423, SN5425 Circuits	
	SN7423, SN7425 Circuits	
Storage temperature range		65°C to 150°C

NOTES: 1. Voltage values, except interemitter voltage, are with respect to network ground terminal.

This is the voltage between two emitters of a multiple-emitter transistor.

recommended operating conditions

			'23 , '25			
			MIN	NOM	MAX	UNIT
		54 Family	4.5	5	5.5	V
VCC	Supply voltage	74 Family	4.75	5	5.25	V
VIH	High-level input voltage		2	-		V
VIL	Low-level input voltage				0.8	٧
Іон	High-level output current				- 0.8	mA
		54 Family			16	mΑ
OF	Low-level output current	74 Family			16	, ina
_		54 Family	- 55		125	°c
T_A	Operating free-air temperature range	74 Family	0		70	ر

The '23 is designed for use with up to four '60 expanders.



electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PAF	AMETER		TEST CO	NDITIONS†		MIN	TYP ‡	MAX	UNIT
V _I		V _{CC} = MIN,	I _I = — 12 mA					- 1.5	V
Voн		V _{CC} = MIN,	V _{IL} = 0.8 V,	I _{OH} = - 0.8 mA		2.4	3.4		٧
VOL		V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 16 mA			0.2	0.4	V
I _I		V _{CC} = MAX,	V1 = 5.5 V					1	mΑ
<u> </u>	data inputs	V _{CC} = MAX,	V ₁ = 2.4 V					40	μА
1H	strobe inputs	VCC - MAA,					160	#7	
	data inputs		W - MAY W - OAN				- 1.6		
ΗL	strobe inputs	V _{CC} = MAX, V ₁ = 0.4 V				- 6.4			mA_
		\(MAY			54 Family	- 20		- 55	
os§		V _{CC} = MAX			74 Family	– 18		– 55	mA
ССН		V _{CC} = MAX,	All inputs at 0	v			8	16	mΑ
ICCL		V _{CC} = MAX,	All inputs at 5	V	· · · · · · · · · · · · · · · · · · ·		10	19	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type. Expander inputs X and \overline{X} are open.

electrical characteristics (SN5423 circuits) using expander inputs, $V_{CC} = 4.5 \text{ V}$, $T_A = -55^{\circ}\text{C}$

	PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
<u>ال</u> ا	Expander current	V _X x = 0.4 V,	I _{OL} = 16 mA				- 3.5	mΑ
VBE(Q)	Base-Emitter voltage of output transistor (Q)	I _{OL} = 16 mA,	$I_X + I_X^{-} = 0.41 \text{ mA},$	$R_{X\overline{X}} = 0$			1.1	٧
Vон	High-level output voltage	$1_{OH} = -0.4 \text{ mA},$	I _X = 0.15 mA,	$I\vec{\chi} = -0.15 \text{ mA}$	2.4	3.4		V
VOL	Low-level output voltage	IOL = 16 mA,	$I_X + I_{\overline{X}} = 0.3 \text{ mA},$	R _X = 114 Ω		0.2	0.4	V

electrical characteristics (SN7423 circuits) using expander inputs, V_{CC} = 4.75 V, T_A = 0°C

	PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
ıχ	Expander current	V _X \overline{\overline{X}} = 0.4 \overline{V},	I _{OL} = 16 mA				– 3.8	mΑ
VBE(Q)	Base-Emitter voltage of output transistor (Q)	I _{OL} = 16 mA,	IX + IX = 0.62 mA,	$R_{XX} = 0$		-	1	٧
Voн	High-level output voltage	I _{OH} = - 0.4 mA,	I _X = 0.27 mA,	1 √ = → 0.27 mA	2.4	3.4		٧
VOL	Low-level output voltage	!OL= 16 mA,	$1_{X} + 1_{X} = 0.43 \text{ mA},$	$H_{XX} = 130 \Omega$		0.2	0.4	V

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics, V_{CC} = 5 V, T_A = 25°C, N = 10, (see note 3)

PARAMETER	TEST CONDITIONS	MIN TYP	MAX	UNIT
tPLH t	$R_{\perp} = 400 \Omega$, $C_{\perp} = 15 pF$	13	22	nş
tPHL	$R_L = 400 \Omega$, $C_L = 15 \rho F$	8	15	ns

NOTE 3: Switching characteristics of the SN5423 and SN7424 are tested with the expander pins open.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_{A} = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time.

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