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- High Capacitive-Drive Capability
- ALS805A Has Typical Delay Time of 4.2 ns (C_L = 50 pF) and Typical Power Dissipation of 4.2 mW Per Gate
- 'AS805B Has Typical Delay Time of 2.6 ns (C_L = 50 pF) and Typical Power Dissipation of 12 mW Per Gate
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

description

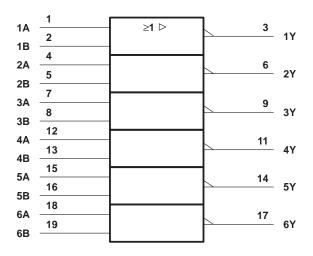
These devices contain six independent 2-input NOR drivers. They perform the Boolean functions $Y = \overline{A} + \overline{B}$ or $Y = \overline{A} \bullet \overline{B}$ in positive logic.

The SN54ALS805A and SN54AS805B are characterized for operation over the full military temperature range of -55° C to 125° C. The SN74ALS805A and SN74AS805B are characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each driver)

INP	UTS	OUTPUT
Α	В	Y
н	Х	L
Х	Н	L
L	L	н

logic symbol[†]



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

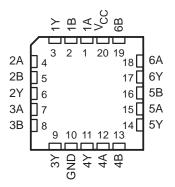
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



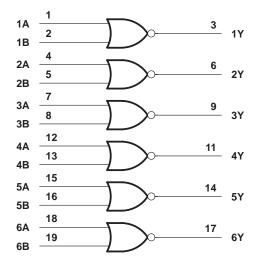
SN54ALS805A, SN54AS805B ... J PACKAGE

SN74ALS805A, SN74AS805B . . . DW OR N PACKAGE

SN54ALS805A, SN54AS805B . . . FK PACKAGE (TOP VIEW)



logic diagram (positive logic)



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V _{CC}	
Operating free-air temperature range, T _A : SN54ALS805A	−55°C to 125°C
SN74ALS805A	0°C to 70°C
Storage temperature range	65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54ALS805A SN74ALS805A		UNIT				
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
ЮН	High-level output current			-12			-15	mA
IOL	Low-level output current			12			24	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST C	TEST CONDITIONS			SN54ALS805A			5A	UNIT
PARAMETER	TEST C	UNDITIONS	MIN	typ‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	lı = –18 mA			-1.2			-1.2	V
	$V_{CC} = 4.5 V \text{ to } 5.5 V,$	I _{OH} = -0.4 mA	V _{CC} -2	2		V _{CC} -2	2		
Vou		I _{OH} = -3 mA	2.4	3.2		2.4	3.2		v
VOH	$V_{CC} = 4.5 V$	$I_{OH} = -12 \text{ mA}$	2						v
		I _{OH} = -15 mA				2			
Ve		I _{OL} = 12 mA		0.25	0.4		0.25	0.4	0.4 V
VOL	V _{CC} = 4.5 V	I _{OL} = 24 mA					0.35	0.5	v
lj	V _{CC} = 5.5 V,	V _I = 7 V			0.1			0.1	mA
Iн	V _{CC} = 5.5 V,	V _I = 2.7 V			20			20	μΑ
١ _{IL}	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
۱ _O §	V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA
ІССН	V _{CC} = 5.5 V,	$V_{I} = 0$		2	4		2	4	mA
ICCL	V _{CC} = 5.5 V,	V _I = 4.5 V		8	14		8	14	mA

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

\$ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.



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switching characte	eristics (see Figure 1)						
PARAMETER	FROM (INPUT)	ТО (ОИТРИТ)	V(Cl Rl T¢	UNIT			
			SN54AL	S805A	SN74AL	S805A	
			MIN	MAX	MIN	MAX	
^t PLH	A or B	V	1	12	2	7	ns
^t PHL		Y	1	9	2	8	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[‡]

Operating free-air temperature range, TA:	SN54AS805B	-55°C to 125°C
	SN74AS805B	

‡ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions§

		SN	54AS80	5B	SN74AS805B		UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
IOH	High-level output current			-40			-48	mA
IOL	Low-level output current			40			48	mA
Т _А	Operating free-air temperature	-55		125	0		70	°C

§ These high sink- or source-current devices are not recommended for use above 40 MHz.



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST C	TEST CONDITIONS		SN54AS805B		SN7	74AS80	5B	UNIT	
PARAMETER	TEST CO			TYP†	MAX	MIN	TYP†	MAX	UNIT	
VIK	V _{CC} = 4.5 V,	Ij = -18 mA			-1.2			-1.2	V	
	$V_{CC} = 4.5 V \text{ to } 5.5 V,$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2	2		V _{CC} -2				
Vou		$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		v	
VОН	V _{OH} V _{CC} = 4.5 V	$I_{OH} = -40 \text{ mA}$	2						v	
		$I_{OH} = -48 \text{ mA}$				2				
		I _{OL} = 40 mA		0.25	0.5				v	
VOL	V _{CC} = 4.5 V	I _{OL} = 48 mA					0.35	0.5	v	
lj	$V_{CC} = 5.5 V,$	$V_{I} = 7 V$			0.1			0.1	mA	
ΙIΗ	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ	
١	V _{CC} = 5.5 V,	VI = 0.4 V			-0.5			-0.5	mA	
IO‡	V _{CC} = 5.5 V,	V _O = 2.25 V	-50		-200	-50		-200	mA	
Іссн	V _{CC} = 5.5 V,	$V_{I} = 0$		6.5	10		6.5	10	mA	
ICCL	V _{CC} = 5.5 V,	VI = 4.5 V		20	32		20	32	mA	

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. [‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

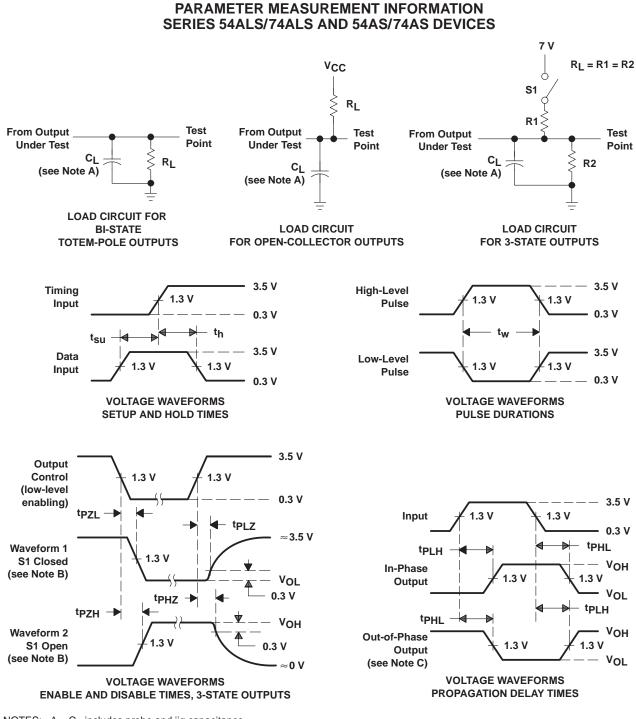
switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	CL RL TA	$\begin{array}{c} V_{CC} = 4.5 \ V \ to \ 5.5 \ V, \\ C_L = 50 \ pF, \\ R_L = 500 \ \Omega, \\ T_A = MIN \ to \ MAX\$ \\ \hline \\ SN54AS805B \ SN74AS80 \\ \end{array}$			UNIT
			MIN	MAX	MIN	MAX	
^t PLH	A or B	v	1	4.8	1	4.3	20
^t PHL	AUB		1	4.8	1	4.3	ns

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



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NOTES: A. CL includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: PRR \leq 1 MHz, t_f = t_f = 2 ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.
 - Figure 1. Load Circuits and Voltage Waveforms



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