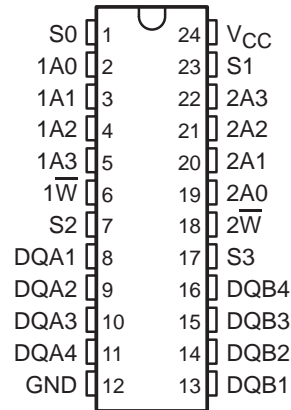


SN74ALS870 DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

- 3-State Buffer-Type Outputs Drive Bus Lines Directly
- Each Register File Has Individual Write-Enable Controls and Address Lines
- Designed Specifically for Multibus Architecture and Overlapping File Operations
- Prioritized B-Input Port Prevents Write Conflicts During Dual-Input Mode
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (NT) 300-mil DIPs

DW OR NT PACKAGE
(TOP VIEW)



description

This device features two 16-word by 4-bit register files. Each register file has individual write-enable ($\overline{1W}$, $\overline{2W}$) controls and address lines. This device has two 4-bit data I/O ports (DQA1–DQA4 and DQB1–DQB4). The data I/O ports can output to bus A and bus B, receive input from bus A and bus B, receive input from bus A and output to bus B, or output to bus A and receive input from bus B. To prevent writing conflicts in the dual-input mode, the B-input port takes priority. Two select (S0 and S1) lines control which port has access to which register. S2 determines whether the A ports are in the input or the output modes and S3 does likewise for the B ports. The address lines (1A0–1A3 or 2A0–2A3) are decoded by an internal 1-of-16 decoder to select which register word is to be accessed. All outputs are 3-state buffer-type outputs designed specifically to drive bus lines directly.

The SN74ALS870 is characterized for operation from 0°C to 70°C.

FUNCTION TABLE

FILE SELECT			INPUT/OUTPUT		
S0	S1	FILE SEL	S2	S3	I/O SEL
L	L	1R to A, 1R to B			
H	L	2R to A, 1R to B	L	L	A out, B out
L	H	1R to A, 2R to B			
H	H	2R to A, 2R to B			
L	L	A to 1R, 1R to B			
H	L	A to 2R, 1R to B	H	L	A in, B out
L	H	A to 1R, 2R to B			
H	H	A to 2R, 2R to B			
L	L	1R to A, B to 1R			
H	L	2R to A, B to 1R	L	H	A out, B in
L	H	1R to A, B to 2R			
H	H	2R to A, B to 2R			
L	L	B to 1R			
H	L	A to 2R, B to 1R	H	H	A in, B in
L	H	A to 1R, B to 2R			
H	H	B to 2R			

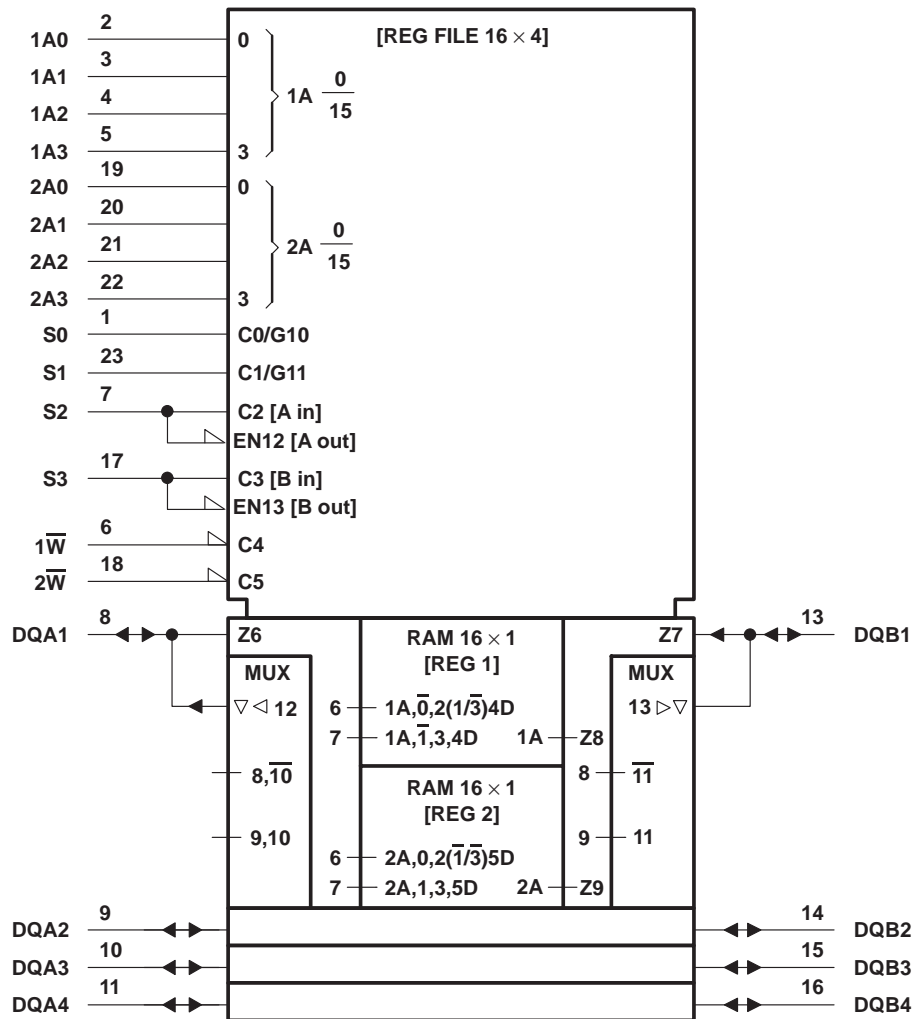
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN74ALS870 DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

logic symbol†

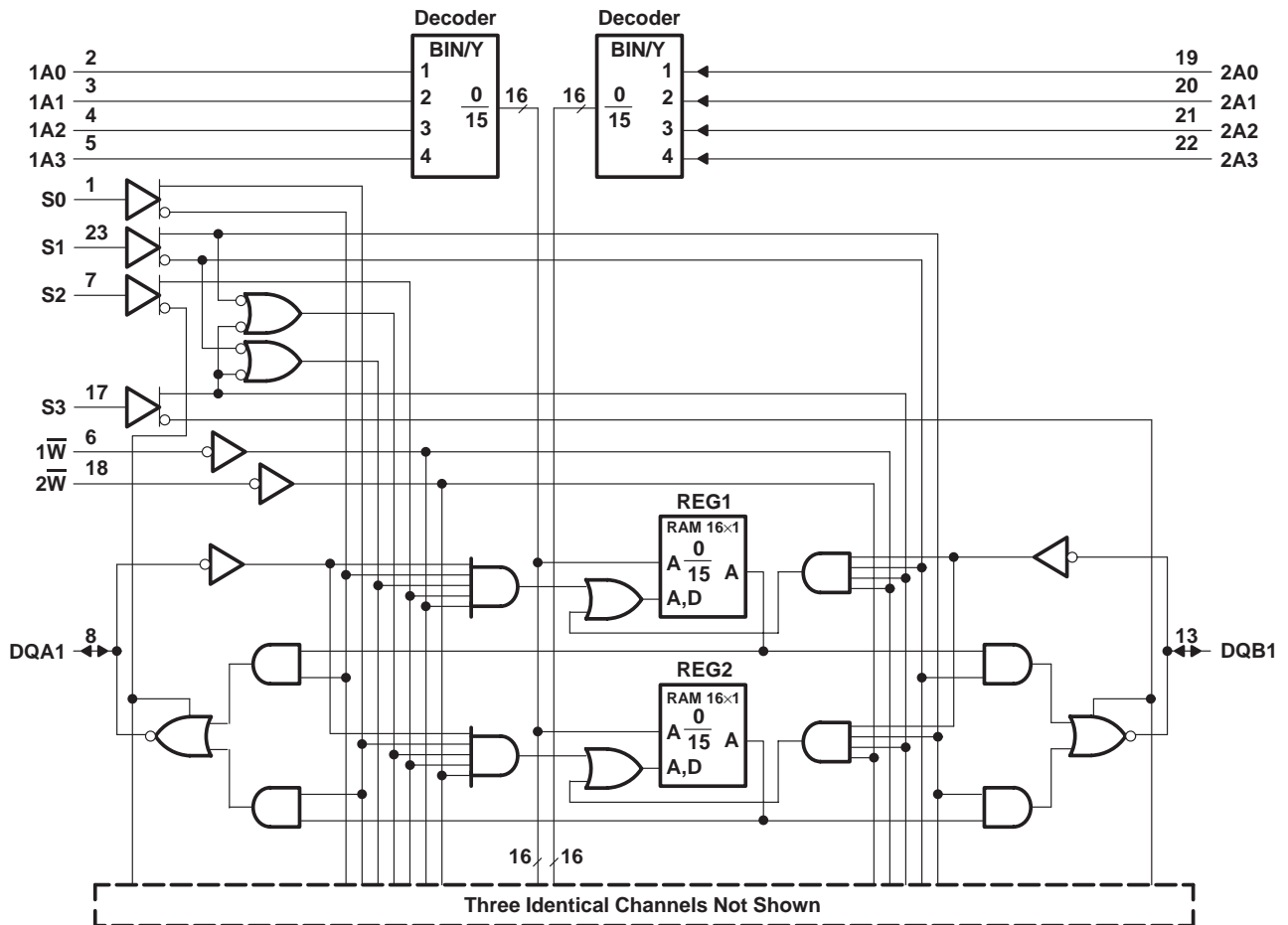


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

SN74ALS870 DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{CC}	7 V
Input voltage, V_I : All inputs	7 V
I/O ports	5.5 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T_A	0°C to 70°C
Storage temperature range	-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SN74ALS870

DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

recommended operating conditions

		MIN	NOM	MAX	UNIT
V _{CC}	Supply voltage	4.5	5	5.5	V
V _{IH}	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
I _{OH}	High-level output current			-2.6	mA
I _{OL}	Low-level output current			24	mA
t _w	Pulse duration, write	12			ns
t _{su}	Setup time	Address before write↓	5		ns
		Data before write↑	15		
		Select before write↓	12		
t _h	Hold time	Address before write↓	0		ns
		Data before write↑	0		
		Select before write↓	12		
T _A	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP†	MAX	UNIT
V _{IK}		V _{CC} = 4.5 V,	I _I = -18 mA			-1.2	V
V _{OH}		V _{CC} = 4.5 V to 5.5 V,	I _{OH} = -0.4 mA	V _{CC} - 2			V
		V _{CC} = 4.5 V,	I _{OH} = -2.6 mA	2.4	3.2		
V _{OL}		V _{CC} = 4.5 V,	I _{OL} = 24 mA	0.35		0.5	V
I _I	Control inputs	V _{CC} = 5.5 V	V _I = 7 V			0.1	mA
	DQA and DQB ports		V _I = 5.5 V			0.2	
I _{IH}	1 \bar{W} and 2 \bar{W}	V _{CC} = 5.5 V,	V _I = 2.7 V			20	μ A
	Other control inputs					40	
	DQA and DQB ports‡					50	
I _{IL}	Control inputs	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.2	mA
	DQA and DQB ports‡					-0.2	
I _O §		V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	mA
I _{CC}		V _{CC} = 5.5 V			80	110	mA

† All typical values are at V_{CC} = 5 V, T_A = 25°C.

‡ For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

§ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SN74ALS870 DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

switching characteristics (see Figure 1)

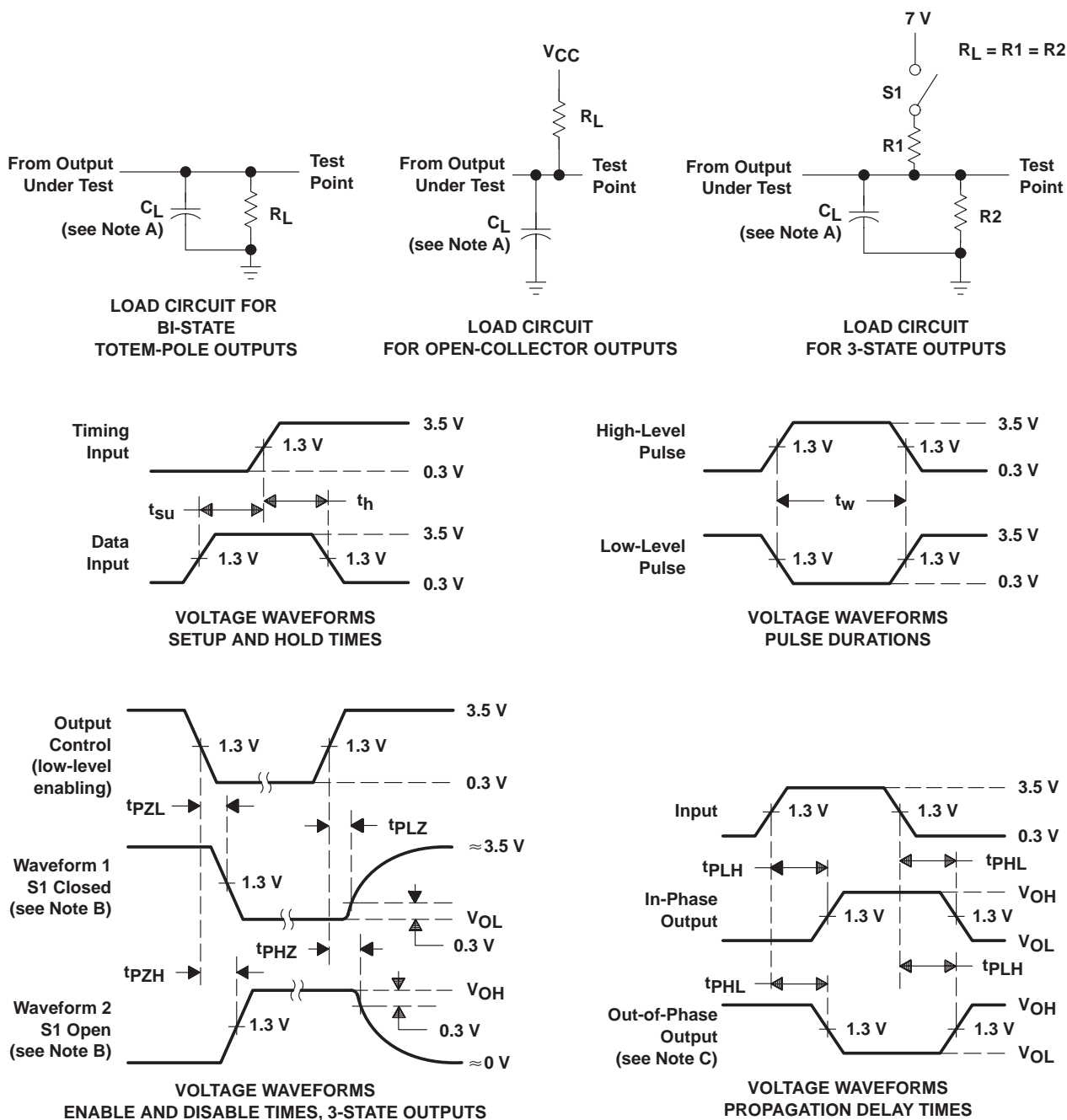
PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R ₁ = 500 Ω, R ₂ = 500 Ω, T _A = MIN to MAX†		UNIT
			MIN	MAX	
t _a (A)	Any A	Any DQ	3	19	ns
t _a (S)	S0	Any DQA	3	15	ns
	S1	Any DQB	3	15	
t _{dis}	S2	Any DQA	3	14	ns
	S3	Any DQB	3	14	
t _{en}	S2	Any DQA	3	17	ns
	S3	Any DQB	3	17	
t _{pd}	\overline{W}	Any DQ	5	23	ns
	DQA	DQB	5	26	
	DQB	DQA	5	26	

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

SN74ALS870 DUAL 16-BY-4 REGISTER FILES

SDAS139A – DECEMBER 1982 – REVISED JANUARY 1995

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



- NOTES: A. C_L includes probe and jig capacitance.
 B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
 D. All input pulses have the following characteristics: $PRR \leq 1$ MHz, $t_r = t_f = 2$ ns, duty cycle = 50%.
 E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.