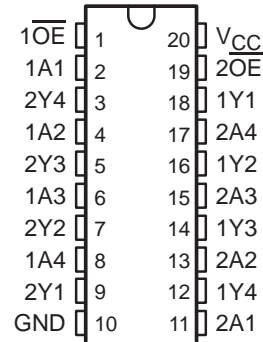


SN74AS230A OCTAL BUFFER/DRIVER WITH 3-STATE OUTPUTS

SDAS213B – DECEMBER 1982 – REVISED DECEMBER 1994

- True and Complementary Outputs
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- High Capacitive-Drive Capability
- Current-Sinking Capability Up to 64 mA
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (N) 300-mil DIPs

DW OR N PACKAGE
(TOP VIEW)



description

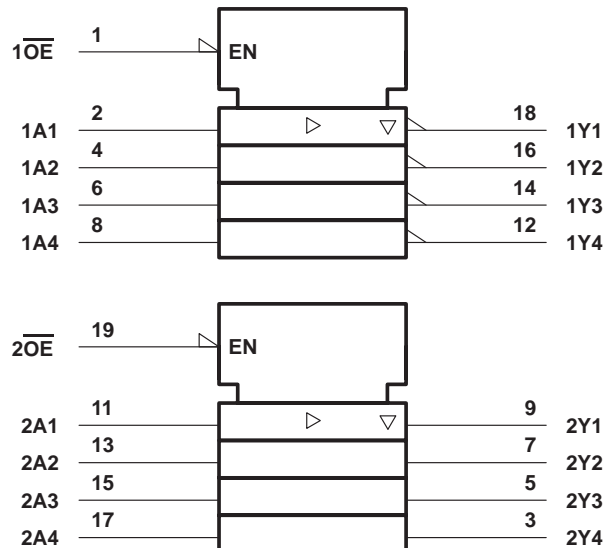
This octal buffer/driver is designed specifically to improve the performance of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. When used together, multiples of this device provide the choice of selected combinations of inverting and noninverting outputs, symmetrical active-low output-enable (\overline{OE}) inputs, and complementary OE and \overline{OE} inputs.

The SN74AS230A is characterized for operation from 0°C to 70°C.

FUNCTION TABLE
(each buffer)

INPUTS		OUTPUT
\overline{OE}	A	Y
L	H	L
L	L	H
H	X	Z

logic symbol†

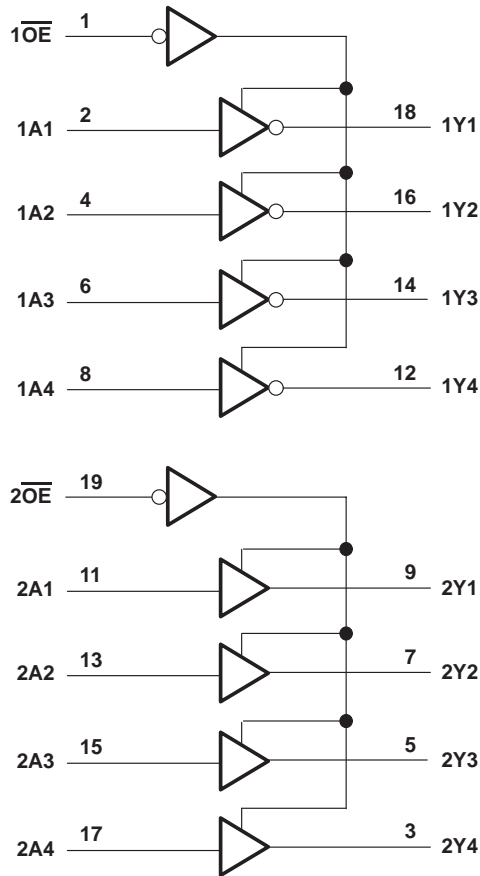


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

SN74AS230A OCTAL BUFFER/DRIVER WITH 3-STATE OUTPUTS

SDAS213B – DECEMBER 1982 – REVISED DECEMBER 1994

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V_{CC}	7 V
Input voltage, V_I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T_A	0°C to 70°C
Storage temperature range	-65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

	MIN	NOM	MAX	UNIT
V_{CC} Supply voltage	4.5	5	5.5	V
V_{IH} High-level input voltage	2			V
V_{IL} Low-level input voltage			0.8	V
I_{OH} High-level output current			-15	mA
I_{OL} Low-level output current			64	mA
T_A Operating free-air temperature	0		70	°C



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SN74AS230A OCTAL BUFFER/DRIVER WITH 3-STATE OUTPUTS

SDAS213B – DECEMBER 1982 – REVISED DECEMBER 1994

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP†	MAX	UNIT
V _{IK}		V _{CC} = 4.5 V,	I _I = -18 mA			-1.2	V
V _{OH}		V _{CC} = 4.5 V to 5.5 V, I _{OH} = -2 mA		V _{CC} - 2			V
		V _{CC} = 4.5 V	I _{OH} = -3 mA	2.4	3.4		
			I _{OH} = -15 mA	2.4			
V _{OL}		V _{CC} = 4.5 V,	I _{OL} = 64 mA	0.31	0.55		V
I _{OZH}		V _{CC} = 5.5 V,	V _O = 2.7 V			50	μA
I _{OZL}		V _{CC} = 5.5 V,	V _O = 0.4 V			-50	μA
I _I		V _{CC} = 5.5 V,	V _I = 7 V			0.1	mA
I _{IH}		V _{CC} = 5.5 V,	V _I = 2.7 V			20	μA
I _{IL}	2A inputs	V _{CC} = 5.5 V,	V _I = 0.4 V			-1	mA
	All other inputs					-0.5	
I _{O‡}		V _{CC} = 5.5 V,	V _O = 2.25 V	-50		-150	mA
I _{CC}		V _{CC} = 5.5 V	Outputs high		16	25	mA
			Outputs low		55	87	
			Outputs disabled		29	46	

† All typical values are at V_{CC} = 5 V, T_A = 25°C.

‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R ₁ = 500 Ω, R ₂ = 500 Ω, T _A = MIN to MAX§		UNIT
			MIN	MAX	
t _{PLH}	1A	1Y	2	6.5	ns
t _{PHL}			1	5.7	
t _{PLH}	2A	2Y	2	6.2	ns
t _{PHL}			1	6.2	
t _{PZH}	1 $\overline{O}E$	1Y	2	6.4	ns
t _{PZL}			2	8.5	
t _{PHZ}	1 $\overline{O}E$	1Y	2	6	ns
t _{PLZ}			2	9.5	
t _{PZH}	2 $\overline{O}E$	2Y	2	9	ns
t _{PZL}			2	7.5	
t _{PHZ}	2 $\overline{O}E$	2Y	2	6	ns
t _{PLZ}			2	9	

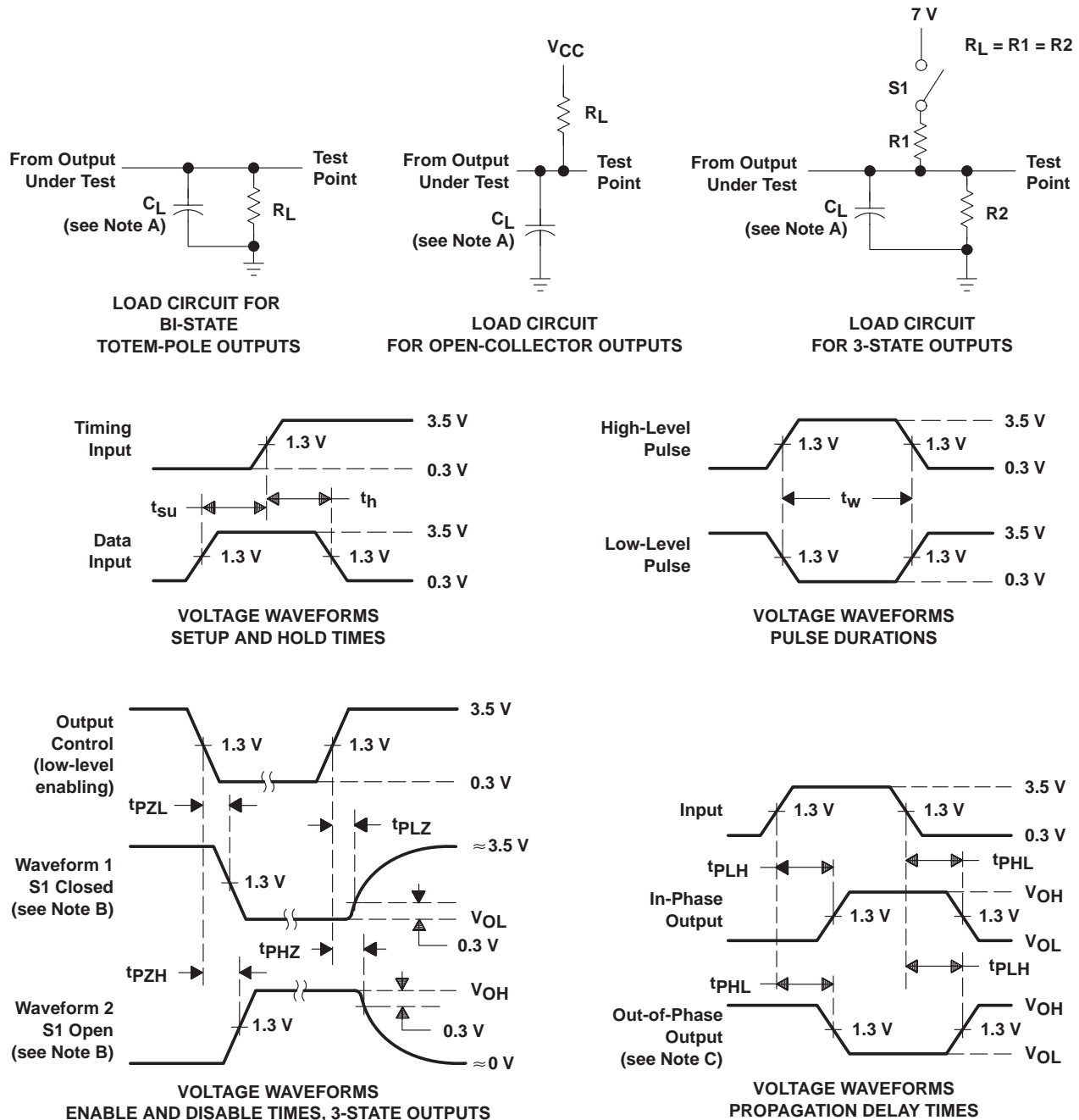
§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



SN74AS230A OCTAL BUFFER/DRIVER WITH 3-STATE OUTPUTS

SDAS213B – DECEMBER 1982 – REVISED DECEMBER 1994

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



- NOTES: A. C_L includes probe and jig capacitance.
 B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
 D. All input pulses have the following characteristics: $PRR \leq 1$ MHz, $t_r = t_f = 2$ ns, duty cycle = 50%.
 E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.