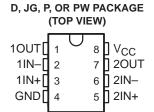
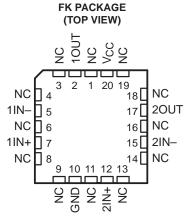
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage ... 2 V to 36 V
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.4 mA Typ Per Comparator
- Low Input Bias Current . . . 25 nA Typ
- Low Input Offset Current . . . 3 nA Typ (LM193)
- Low Input Offset Voltage . . . 2 mV Typ
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

#### description

These devices consist of two independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as





NC - No internal connection

long as the difference between the two supplies is 2 V to 36 V, and  $V_{CC}$  is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

The LM193 is characterized for operation from –55°C to 125°C. The LM293 and LM293A are characterized for operation from –25°C to 85°C. The LM393 and LM393A are characterized for operation from 0°C to 70°C. The LM2903 and LM2903Q are characterized for operation from –40°C to 125°C and are manufactured to demanding automotive requirements.

#### logic diagram (each comparator)





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



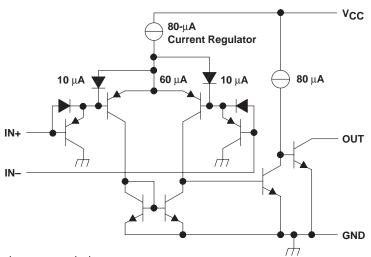
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#### **AVAILABLE OPTIONS**

		PACKAGED DEVICES							
TA	V <sub>IO(max</sub> ) AT 25°C	SMALL OUTLINE (D) <sup>†</sup>	CHIP CARRIER (FK)	CERAMIC DIP (JG)	PLASTIC DIP (P)	TSSOP (PW) <sup>‡</sup>	CHIP FORM (Y)§		
0°C to 70°C	5 mV	LM393D	_	_	LM393P	LM393PW	LM393Y		
0-0 10 70-0	2 mV	LM393AD	_	_	LM393AP	_	_		
–25°C to 85°C	5 mV	LM293D	_	_	LM293P	_	_		
-25°C 10 85°C	2 mV	LM293AD	_	_	LM293AP	_	_		
-40°C to 125°C	7 mV	LM2903D	_	_	LM2903P	LM2903PW	_		
=40°C to 125°C	7 1110	LM2903QD	_	_	LM2903QP	_	_		
–55°C to 125°C	5 mV	LM193D	LM193FK	LM193JG	LM193P	_	_		

<sup>†</sup> The D package is available taped and reeled. Add the suffix R (e.g., LM393DR).

#### schematic (each comparator)



COMPONENT	COUNT
Epi-FET	1
Diodes	2
Resistors	2
Transistors	30

Current values shown are nominal.

<sup>&</sup>lt;sup>‡</sup> The PW package is only available left-end taped and reeled (e.g., LM393PWR).

<sup>§</sup> Chips are tested at 25°C (see LM393Y for electrical characteristics).

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#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC</sub> (see Note 1)	
Differential input voltage, V <sub>ID</sub> (see Note 2)	
Output voltage, V <sub>O</sub>	
Output current, I <sub>O</sub>	
Duration of output short-circuit to ground (see Note 3)	
Continuous total power dissipation	See Dissipation Rating Table
Package thermal impedance, θ <sub>JA</sub> (see Note 4): D package	97°C/W
P package	85°C/W
PW package	149°C/W
Case temperature for 60 seconds: FK package	260°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D, P, or PW pa	ackage 260°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: JG package .	300°C
Storage temperature range, T <sub>stq</sub>	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to the network ground.

- 2. Differential voltages are at IN+ with respect to IN-.
- 3. Short circuits from outputs to  $V_{\hbox{\scriptsize CC}}$  can cause excessive heating and eventual destruction.
- 4. The package thermal impedance is calculated in accordance with JESD 51.

#### **DISSIPATION RATING TABLE**

PACKAGE	$T_{\mbox{$A$}} \leq 25^{\circ}\mbox{$C$}$ POWER RATING	DERATING FACTOR	DERATE ABOVE T <sub>A</sub>	T <sub>A</sub> = 70°C POWER RATING	T <sub>A</sub> = 85°C POWER RATING	T <sub>A</sub> = 125°C POWER RATING
FK	900 mW	11.0 mW/°C	68°C	880 mW	715 mW	275 mW
JG	900 mW	8.4 mW/°C	43°C	672 mW	546 mW	210 mW



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# electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		τ <sub>A</sub> †	LM193		LM293 LM393			UNIT	
					MIN	TYP	MAX	MIN	TYP	MAX	
\/. ~	lanut offeet veltere	V <sub>CC</sub> = 5 V to 30	) V,	25°C		2	5		2	5	mV
VIO	Input offset voltage	$V_O = 1.4 \text{ V},$ $V_{IC} = V_{IC(min)}$		Full range			9			9	IIIV
1,0	Input offset current	VO = 1.4 V		25°C		3	25		5	50	nA
lio	input onset current	VO = 1.4 V		Full range			100			250	IIA
l.p	Input bias current	VO = 1.4 V		25°C		-25	-100		-25	-250	nA
IB	input bias current	VO = 1.4 V		Full range			-300			-400	ш
\/.a=	Common-mode			25°C	0 to V <sub>CC</sub> -1.5			0 to V <sub>CC</sub> -1.5			V
VICR	input voltage range‡			Full range	0 to V <sub>CC</sub> -2			0 to V <sub>CC</sub> -2			V
AVD	Large-signal differential voltage amplification	$V_{CC}$ = 15 V, $V_{O}$ = 1.4 V to 1 $R_{L}$ ≥ 15 k $\Omega$ to V		25°C	50	200		50	200		V/mV
	High-level	V <sub>OH</sub> = 5 V,	V <sub>ID</sub> = 1 V	25°C		0.1			0.1	50	nA
ЮН	output current	V <sub>OH</sub> = 30 V,	V <sub>ID</sub> = 1 V	Full range			1			1	μΑ
\/o.	Low-level	IOI = 4 mA,	V <sub>ID</sub> = -1 V	25°C		150	400		150	400	mV
VOL	output voltage	IOL = 4 IIIA,	νID = -1 Λ	Full range			700			700	IIIV
loL	Low-level output current	V <sub>OL</sub> = 1.5 V,	V <sub>ID</sub> = 1 V	25°C	6			6			mA
laa	Cumply ourrent	B	V <sub>CC</sub> = 5 V	25°C		0.8	1		0.8	1	mΛ
Icc	Supply current	R <sub>L</sub> = ∞	V <sub>CC</sub> = 30 V	Full range			2.5			2.5	mA

<sup>†</sup> Full range (MIN or MAX) for LM193 is –55°C to 125°C, for LM293 is 25°C to 85°C, and for LM393 is 0°C to 70°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

<sup>&</sup>lt;sup>‡</sup> The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V<sub>CC+</sub> –1.5 V, but either or both inputs can go to 30 V without damage.

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## electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T <sub>A</sub> †		LM293A LM393A			LM2903 LM2903Q		
					MIN	TYP	MAX	MIN	TYP	MAX	
1/1-2	lanut effect voltere	V <sub>CC</sub> = 5 V to 3	0 V,	25°C		1	3		2	7	mV
VIO	Input offset voltage	$V_O = 1.4 \text{ V},$ $V_{IC} = V_{IC(min)}$		Full range			4			15	IIIV
l.o	Input offset current	Vo = 1.4.V		25°C		5	50		5	50	nA
110	input onset current	V <sub>O</sub> = 1.4 V		Full range			150			200	IIA
lin.	Input bias current	V <sub>O</sub> = 1.4 V		25°C		-25	-250		-25	-250	nA
IB	input bias current	VO = 1.4 V		Full range			-400			-500	IIA
	Common-mode			25°C	0 to V <sub>CC</sub> -1.5			0 to V <sub>CC</sub> -1.5			٧
VICR	input voltage range‡			Full range	0 to V <sub>CC</sub> -2			0 to V <sub>CC</sub> -2			V
AVD	Large-signal differential voltage amplification	$V_{CC} = 15 \text{ V},$ $V_{O} = 1.4 \text{ V to 1}$ $R_{L} \ge 15 \text{ k}\Omega \text{ to V}$		25°C	50	200		25	100		V/mV
la	High-level	V <sub>OH</sub> = 5 V,	V <sub>ID</sub> = 1 V	25°C		0.1	50		0.1	50	nA
ЮН	output current	V <sub>OH</sub> = 30 V,	V <sub>ID</sub> = 1 V	Full range			1			1	μΑ
\/o.	Low-level	IOI = 4 mA,	V <sub>ID</sub> = -1 V	25°C		150	400		150	400	mV
VOL	output voltage	IOL = 4 IIIA,	νID = -1 ν	Full range			700			700	IIIV
loL	Low-level output current	V <sub>OL</sub> = 1.5 V,	V <sub>ID</sub> = 1 V	25°C	6			6			mA
loo	Supply current	R <sub>I</sub> = ∞	V <sub>CC</sub> = 5 V	25°C		0.8	1		0.8	1	mA
lcc	Supply current	NL = ∞	V <sub>CC</sub> = 30 V	Full range			2.5			2.5	IIIA

<sup>†</sup> Full range (MIN or MAX) for LM293A is 25°C to 85°C, for LM393A is 0°C to 70°C, and for LM2903 and LM2903Q is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

# electrical characteristics at $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

PARAMETER		TEST CO	NUDITIONS	LM393Y			UNIT
	PARAMETER	TEST CONDITIONS		MIN	TYP§	MAX	UNIT
V <sub>IO</sub>	Input offset voltage				2	5	mV
IIO	Input offset current	$V_{CC} = 5 \text{ V to } 30 \text{ V},$ $V_{C} = 1.4 \text{ V}$	VIC = VICR(min),		5	50	nA
I <sub>IB</sub>	Input bias current	VO = 1.4 V			-25	-250	nA
VICR	Common-mode input voltage range	V <sub>CC</sub> = 5 V to 30 V		0 to V <sub>CC</sub> -1.5	5		V
AVD	Large-signal differential voltage amplification	$V_{CC} = 15 \text{ V},$ $R_L \ge 15 \text{ k}\Omega \text{ to } V_{CC}$	$V_0 = 1.4 \text{ V to } 11.4 \text{ V},$	25	200		V/mV
ІОН	High-level output current	V <sub>OH</sub> = 5 V,	V <sub>ID</sub> = 1 V		0.1	50	nA
VOL	Low-level output voltage	I <sub>OL</sub> = 4 mA,	V <sub>ID</sub> = -1 V		150	400	mV
loL	Low-level output current	V <sub>OL</sub> = 1.5 V,	V <sub>ID</sub> = -1 V	6			mA
ICC	Supply current	R <sub>L</sub> = ∞,	V <sub>CC</sub> = 5 V		0.8	1	mA

<sup>§</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified.



<sup>&</sup>lt;sup>‡</sup> The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is  $V_{CC+}$  –1.5 V, but either or both inputs can go to 30 V without damage.

# LM193, LM293, LM293A, LM393 LM393A, LM393Y, LM2903, LM2903Q DUAL DIFFERENTIAL COMPARATORS SLCS005E – JUNE 1976 – REVISED NOVEMBER 1999

# switching characteristics, $V_{CC}$ = 5 V, $T_A$ = 25°C

PARAMETER TEST CONDITIONS	ONDITIONS	LM193 LM293, LM293A LM393, LM393A LM2903, LM2903Q			UNIT	
			MIN	TYP	MAX	
R <sub>L</sub> connected to 5 V through 5.1 kΩ,		100-mV input step with 5-mV overdrive		1.3		นร
Response time	$C_L = 15 \text{ pF}^{\dagger}$ , See Note 5 TTL-level input step			0.3		

† C<sub>L</sub> includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



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