



## SILICON PLASTIC POWER TRANSISTOR PNP TIP32C 3A 40W

### Technical Data

...designed for use in general-purpose switching and amplifier applications.

- ☛ DC Current Gain -  $h_{FE} = 10-50$  @  $I_C = 3.0\text{Adc}$
- ☛ Collector-Emitter Saturation Voltage –  $V_{CE(\text{sat})} = 1.2\text{Vdc}$  (Max) @  $I_C=3\text{Adc}$
- ☛ TO-220 Package
- ☛ Collector-Emitter Sustaining Voltage –  $V_{CEO(\text{sus})} = 100\text{Vdc}$  (Min)

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector- Emitter Voltage	$V_{CEO}$	100	Vdc
Collector – Base Voltage	$V_{CB}$	100	Vdc
Emitter Base Voltage	$V_{EB}$	5	Vdc
Collector Current – Continuos Peak	$I_C$	3 5	Adc
Base Current	$I_B$	1	Adc
Total Power Dissipation @ $TC = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	PD	40 0.32	Watts $\text{W}/^\circ\text{C}$
Operating and Storage junction Temperature Range	$T_j, T_{stg}$	-65 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max.	Unit
Thermal resistance junction to case	$R_{thjc}$	3.125	°C/W



**ELECTRICAL CHARACTERISTICS :[  $T_c = 25^\circ\text{C}$  unless otherwise noted ]**

Characteristic	Symbol	Min	Typ	Max	Unit
<b>* OFF CHARACTERISTICS :</b>					
Collector-Emitter Sustaining Voltage(1) [ $I_c = 30 \text{ mA}$ , $I_B = 0$ ]	$V_{CEO(\text{sus})}$	100			Vdc
Collector Cutoff Current [ $V_{CE} = 60 \text{ Vdc}$ , $I_B = 0$ ]	$I_{CE0}$			0..3	mA
Collector Cutoff Current [ $V_{CE} = 100 \text{ Vdc}$ , $V_{BE} = 0$ ]	$I_{CES}$			200	mA
Emitter Cutoff Current [ $V_{BE} = 5.0 \text{ Vdc}$ , $I_c = 0$ ]	$I_{EBO}$			1	mA
<b>* ON CHARACTERISTICS (1):</b>					
DC Current Gain [ $I_c = 1.0 \text{ Adc}$ , $V_{CE} = 4.0 \text{ Vdc}$ ] [ $I_c = 3 \text{ Adc}$ , $V_{CE} = 4.0 \text{ Vdc}$ ]	$h_{FE}$	25 10		---	50
Collector-Emitter Saturation Voltage [ $I_c = 3 \text{ Adc}$ , $I_B = 375 \text{ mA}$ ]	$V_{CE(\text{sat})}$			1.2	Vdc
Base-Emitter on Voltage [ $I_c = 3.0 \text{ Adc}$ , $V_{CE} = 4.0 \text{ V}_D$ ]	$V_{BE(\text{on})}$			1.8	Vdc
<b>DYNAMIC CHARACTERISTICS :</b>					
Current Gain – Bandwidth Product [ $I_c = 0.5 \text{ Adc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f_{\text{test}} = 1.0 \text{ MHz}$ ]	$f_T$	3			MHz
Small-Signal Current Gain [ $I_c = 0.5 \text{ Adc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1 \text{ kHz}$ ]	$h_{fe}$	20			

- Indicates within JEDEC Registration Data.
- (1) Pulse Test : Pulse Width < 300μs , Duty Cycle < 2.0%