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TONE/PULSE DIALER WITH HANDFREE AND HOLD FUNCTIONS

GENERAL DESCRIPTION

The W91520N series are Si-gate CMOS ICs that provide the necessary signals for tone or pulse dialing. The W91520N series provide one-key redial, handfree dialing, hold, save, and redial functions.
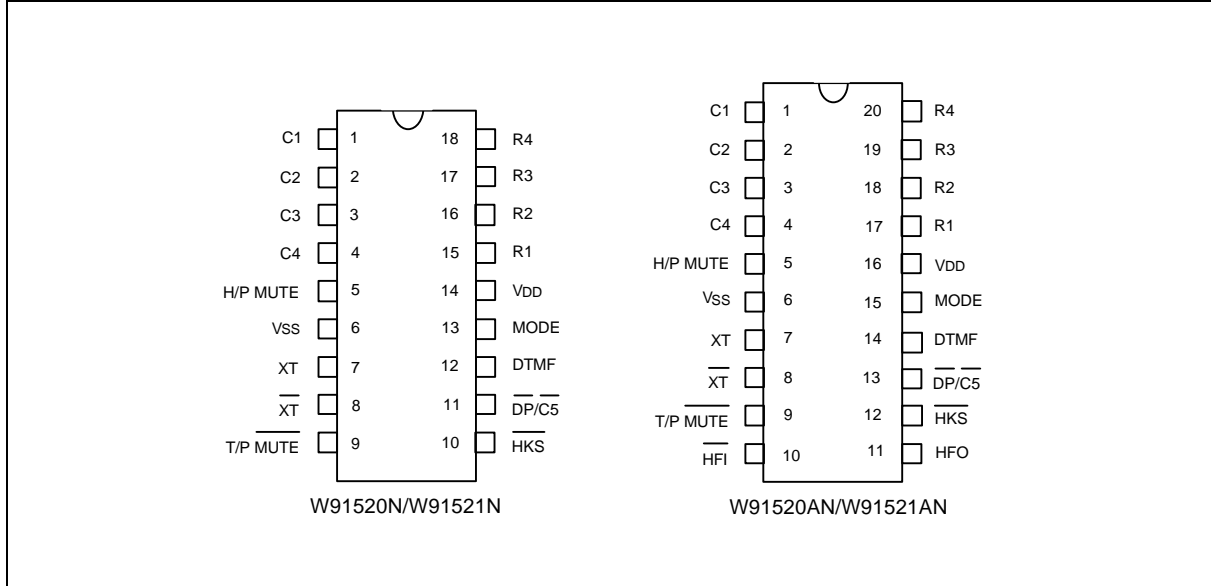
FEATURES

- DTMF/pulse switchable dialer
- Two by 32-digit redial and save memory
- Pulse-to-tone (*/T) keypad for long distance call operation
- Uses 5×5 keyboard
- Easy operation with redial, flash, pause, and */T keypads
- Pause, pulse-to-tone (*/T) can be stored as a digit in memory
- On-hook debounce time: 150 mS
- Dialing rate (10 ppS, or 20 ppS) selectable by bonding option
- Minimum tone output duration: 93 msec.
- Minimum intertone pause: 93 msec.
- Flash break time (73, 100, 300, 600 msec.) selectable by keypad; pause time is 1.0 sec.
- On-chip power-on reset
- Uses 3.579545 MHz crystal or ceramic resonator
- Packaged in 18 or 20-pin plastic DIP
- The different dialers in the W91520N series are shown in the following table:

TYPE NO.	REPLACEMENT TYPE NO.	PULSE (ppS)	FLASH (mS)	M/B	HANDFREE DIALING	PACKAGE (PINS)
W91520N	W91520	10	600/100/300/73	Pin	-	18
W91521N	W91521	20	600/100/300/73	Pin	-	18
W91520AN	W91520A	10	600/100/300/73	Pin	Yes	20
W91521AN	W91521A	20	600/100/300/73	Pin	Yes	20

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PIN CONFIGURATIONS

C1	1	18	R4
C2	2	17	R3
C3	3	16	R2
C4	4	15	R1
H/P MUTE	5	14	VDD
VSS	6	13	MODE
XT	7	12	DTMF
$\overline{\text{XT}}$	8	11	$\overline{\text{DP/C5}}$
T/P MUTE	9	10	HKS

W91520N/W91521N

C1	1	20	R4
C2	2	19	R3
C3	3	18	R2
C4	4	17	R1
H/P MUTE	5	16	VDD
VSS	6	15	MODE
XT	7	14	DTMF
$\overline{\text{XT}}$	8	13	$\overline{\text{DP/C5}}$
T/P MUTE	9	12	$\overline{\text{HKS}}$
$\overline{\text{HFI}}$	10	11	HFO

W91520AN/W91521AN

PIN DESCRIPTION

SYMBOL	18-PIN	20-PIN	I/O	FUNCTION
Column- Row Inputs	1–4 & 15–18	1–4 & 17–20	I	The keyboard inputs may be used with either a standard 5×5 keyboard or an inexpensive single contact (Form A) keyboard. Electronic input from a μC can also be used. A valid key-in is defined as a single row being connected to a single column.
XT, $\overline{\text{XT}}$	7, 8	7, 8	I, O	A built-in inverter provides oscillation with an inexpensive 3.579545 MHz crystal or ceramic resonator.
T/P $\overline{\text{MUTE}}$	9	9	O	The T/P $\overline{\text{MUTE}}$ is a conventional CMOS N-channel open drain output. The output transistor is switched on during dialing sequence, one-key redial break, and flash break time. Otherwise, it is switched off.
MODE	13	15	I	Pulling mode pin to VSS places the dialer in tone mode. Pulling mode pin to VDD places the dialer in pulse mode (10 ppS; 20 ppS for W91521N/521AN, M/B = 40:60). Floating mode pin places the dialer in pulse mode (10 ppS; 20 ppS for W91521N/521AN, M/B = 33.3:66.7).

Pin Description, continued

SYMBOL	18-PIN	20-PIN	I/O	FUNCTION
$\overline{HKS}$	10	12	I	Hook switch input. $\overline{HKS}$ = VDD: On-hook state. Chip in sleeping mode, no operation. $\overline{HKS}$ = VSS: Off-hook state. Chip is enabled for normal operation. $\overline{HKS}$ pin is pulled to VDD by internal resistor.
$\overline{DP}/\overline{C5}$	11	13	O	N-channel open drain dialing pulse output. Flash key will cause $\overline{DP}$ to be active in either tone mode or pulse mode. The timing diagram for pulse mode is shown in Figure 1(a, b, c).
VDD, VSS	14, 6	16, 6	I	Power input pins.
H/P MUTE	5	5	O	The H/P MUTE is a conventional inverter output. During pulse dialing, flash break, one-key redial break, and hold period, this output is active high; otherwise, it remains in low state.
DTMF	12	14	O	In pulse mode, this pin remains in low state at all times. In the tone mode, it will output a dual or single tone. Detailed timing diagram for tone mode is shown in Figure 2(a, b, c).

Output Frequency			
	Specified	Actual	Error %
R1	697	699	+0.28
R2	770	766	-0.52
R3	852	848	-0.47
R4	941	948	+0.74
C1	1209	1216	+0.57
C2	1336	1332	-0.30
C3	1477	1472	-0.34

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Pin Description, continued

SYMBOL	18-PIN	20-PIN	I/O	FUNCTION
$\overline{\text{HFI}}$, HFO	-	10, 11	I, O	Handfree control pins. The handfree control state is toggled on by a low pulse on the $\overline{\text{HFI}}$ input pin. The status of the handfree control state is described in the following table:

CURRENT STATE		NEXT STATE		
Hook SW.	HFO	Input	HFO	Dialing
−	Low	$\overline{\text{HFI}}$ ⌐L	High	Yes
On Hook	High	$\overline{\text{HFI}}$ ⌐L	Low	No
Off Hook	High	$\overline{\text{HFI}}$ ⌐L	Low	Yes
On Hook	−	Off Hook	Low	Yes
Off Hook	Low	On Hook	Low	No
Off Hook	High	On Hook	High	Yes

$\overline{\text{HFI}}$ pin is pulled to V$_{DD}$ by an internal resistor.

Detailed timing diagrams are shown in Figure 3.

BLOCK DIAGRAM

FUNCTIONAL DESCRIPTION

Keyboard Operation

C1	C2	C3	C4	$\overline{DP}$ / $\overline{C5}$	
1	2	3	SAVE		R1
4	5	6	F1		R2
7	8	9	F2	H	R3
∗/T	0	#	R/P1	R	R4
R/P2	R	F3	F4		Vx

- R/P1, R/P2: Redial and pause function key; P1 is 3.6 sec. and P2 is 2.0 sec.
- ∗/T: ∗ in tone mode and P→T in pulse mode
- F1, ..., F4: Flash keys, flash break time of F1 = 600 mS, F2 = 100 mS, F3 = 300 mS, F4 = 73 mS
- H: Hold function key
- SAVE: Save function key
- R: One-key redial function

Notes:

D1, ..., Dn, D1', ..., Dn': 0, ..., 9, */T, #

R/P: R/P1 or R/P2.

Fn: F1, ..., F4

Normal Dialing

| OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | D1 | , | D2 | , ..., | Dn |

1. D1, D2, ..., Dn will be dialed out.

2. Dialing length is unlimited, but redial is inhibited if length exceeds 32 digits in normal dialing.

Redialing

1. | OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | D1 | , | D2 | , ..., | Dn | , Busy, Come

| ON HOOK | , | OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | R/P |

a. The redial memory content will be dialed out.

b. The | R/P | key can execute the redial function only as the first key-in after off-hook; otherwise, it

executes pause function.

c. If redialing length exceeds 32 digits, the redialing function will be inhibited.

2. | OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | D1 | , | D2 | , ..., | Dn | , Busy, | R |

a. The one-key redialing function timing diagram is shown in Figure 4.

b. If the dialing D1 to Dn is finished, pressing the R key will cause the pulse output pin of to

go low for 2.2 seconds break time and 0.6 seconds pause time will automatically be added.

c. If the pulses of the dialed digits D1 to Dn have not finished, R will be ignored.

d. The redial function by R key has no break time (2.2 sec.) if it is the first key-in after off-hook.

e. The R key uses the same redial buffer as the redial function by R/P1 or R/P2 key, the

and it is active during normal dialing or repertory dialing.

Access Pause

OFF HOOK (or ON HOOK & $\overline{HF}$ ïõ), D1 , D2 , R/P , D3 , ..., Dn

1. The pause function is executed in normal dialing, redial dialing, or memory dialing.

2. The pause duration of 2.0 or 3.6 seconds per pause is selected by keypad, but only one pause time can be stored in memory

3. A detailed timing diagram for the pause function is shown in Figure 5.

Pulse-to-tone (*/T)

OFF HOOK (or ON HOOK & $\overline{HF}$ ïõ), D1 , D2 , ..., Dn , */T , D1'

, D2' , ..., Dn'

1. If the mode switch is set to pulse mode, then the output signal will be:

D1, D2, ..., Dn, Pause (2.0 sec. or 3.6 sec.), D1', D2', ..., Dn'

(Pulse) (Tone)

2. If the mode switch is set to tone mode, then the output signal will be:

D1, D2, ..., Dn, *, D1', D2', ..., Dn'

(Tone) (Tone)

3. The dialer remains in tone mode when the digits have been dialed out and can be reset to pulse mode only by going on-hook.

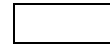4. The pulse-to-tone function timing diagram is shown in Figure 6.

Save

OFF HOOK (or ON HOOK & $\overline{HF}$ ïõ), D1 , D2 , ..., Dn , SAVE

1. D1, D2, ..., Dn will be dialed out.

2. If the sequence of the dialed digits D1, D2, ..., Dn has not SAVE will be ignored;

finished,

   otherwise, D1, D2, ..., Dn will be duplicated to the save memory.

Flash

| OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | Fn |

1. Fn = F1, ..., F4

2. The dialer will execute a flash break time of 600 mS (F1), 100 mS (F2), 300 mS (F3), or 73 mS (F4). In each case the flash pause time is 1.0 sec. before the next digit is dialed out.

3. Flash key cannot be stored as a digit in memory. The flash key has first priority among the keyboard   functions.

4. The system will return to the initial state after the flash pause time is finished.

5. The flash function timing diagram is shown in Figure 7.

HOLD

| OFF HOOK | (or | ON HOOK | & | $\overline{HFI}$ |), | H |

The HOLD is switched on and off by hold key. The other keypads will be disabled when in hold mode.

The function timing diagram is shown in Figure 3(a, b, c).

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT
DC Supply Voltage	$V_{DD}-V_{SS}$	-0.3 to +7.0	V
Input/Output Voltage	V_{IL}	V_{SS} -0.3	V
	V_{IH}	V_{DD} +0.3	V
	V_{OL}	V_{SS} -0.3	V
	V_{OH}	V_{DD} +0.3	V
Power Dissipation	P_D	120	mW
Operation Temperature	T_{OPR}	-20 to +70	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Note: Exposure to conditions beyond those listed under Absolute Maximum Ratings may adversely affect the life and reliability of the device.

DC CHARACTERISTICS

(V$_{DD}$–V$_{SS}$ = 2.5V, F$_{OSC.}$ = 3.579545 MHz, T$_A$ = 25° C, all outputs unloaded)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V$_{DD}$	-	2.0	-	5.5	V
Operating Current	I$_{OP}$	Tone, Unloaded	-	0.4	0.6	mA
		Pulse, Unloaded	-	0.2	0.4	
Standby Current	I$_{SB}$	$\overline{HKS}$ = V$_{SS}$, No load & No key entry	-	-	15	µA
Memory Retention Current	I$_{MR}$	$\overline{HKS}$ = V$_{DD}$, V$_{DD}$ = 1.0V	-	-	0.2	µA
DTMF Output Voltage	V$_{TO}$	Row group, R$_L$ = 5 KΩ	130	150	170	mVrms
Pre-emphasis		Col/Row, V$_{DD}$ = 2.0 to 5.5V	1	2	3	dB
DTMF Distortion	THD	R$_L$ = 5 KΩ, V$_{DD}$ = 2.0 to 5.5V	-	-30	-23	dB
DTMF Output DC Level	V$_{TDC}$	R$_L$ = 5 KΩ, V$_{DD}$ = 2.0 to 5.5V	1.0	-	3.0	V
DTMF Output Sink Current	I$_{TL}$	V$_{TO}$ = 0.5V	0.2	-	-	mA
$\overline{DP}$ Output Sink Current	I$_{PL}$	V$_{PO}$ = 0.5V	0.5	-	-	mA
T/P $\overline{MUTE}$ Output Sink Current	I$_{TML}$	V$_{TMO}$ = 0.5V	0.5	-	-	mA
H/P MUTE Output Drive/Sink Current	I$_{HPH}$	V$_{HPH}$ = 2.0V	0.5	-	-	mA
	I$_{HPL}$	V$_{HPL}$ = 0.5V	0.5	-	-	mA
HFO Drive/Sink Current	I$_{HFH}$	V$_{HFH}$ = 2.0V	0.5	-	-	mA
	I$_{HFL}$	V$_{HFL}$ = 0.5V	0.5	-	-	mA
Keypad Input Drive Current	I$_{KD}$	V$_I$ = 0.0V	30	-	-	µA
Keypad Input Sink Current	I$_{KS}$	V$_I$ = 2.5V	200	400	-	µA
$\overline{HKS}$ I/P Pull-high Resistor	R$_{HK}$	-	-	300	-	KΩ
Keypad Resistance	R$_K$	-	-	-	5	KΩ

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AC CHARACTERISTICS

($V_{DD}-V_{SS}$ = 2.5V, $F_{OSC.}$ = 3.579545 MHz, T_A = 25° C, all outputs unloaded)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Key-in Debounce	T_{KID}	-	-	20	-	mS
Key Release Debounce	T_{KRD}	-	-	20	-	mS
On-hook Debounce	T_{OHD}	-	-	150	-	mS
Pre-digit Pause[1]	T_{PDP1}	Mode = V_{DD}	-	40	-	mS
	10 ppS	Mode = Floating	-	33.3	-	
Pre-digit Pause[2]	T_{PDP2}	Mode = V_{DD}	-	20	-	mS
	20 ppS	Mode = Floating	-	16.7	-	
Interdigit Pause	T_{IDP}	10 ppS	-	800	-	mS
(Auto Dialing)		20 ppS	-	500	-	
Make/Break Ratio	M:B	Mode = V_{DD}	-	40:60	-	%
		Mode = Floating	-	33.3:66.7	-	
Tone Output Duration	T_{TD}	Auto dialing	-	93	-	mS
Intertone Pause	T_{ITP}	Auto dialing	-	93	-	mS
Flash Break Time	T_{FB}	F1	-	600	-	mS
		F2	-	100	-	
		F3		300		
		F4	-	73	-	
Flash Pause Time	T_{FP}	F1, F2, F3, F4	-	1.0	-	S
Pause Time	T_P	R/P1	-	3.6	-	S
		R/P2	-	2.0	-	
One-key Redial Break Time	T_{RB}	-	-	2.2	-	S
One-key Redial Pause Time	T_{RP}	-	-	600	-	mS

Notes:

1. Crystal parameters suggested for proper operation are Rs < 100 Ω, Lm = 96 mH, Cm = 0.02 pF, Cn = 5 pF, Cl = 18 pF, $F_{OSC.}$ = 3.579545 MHz ±0.02%.

2. Crystal oscillator accuracy directly affects these times.
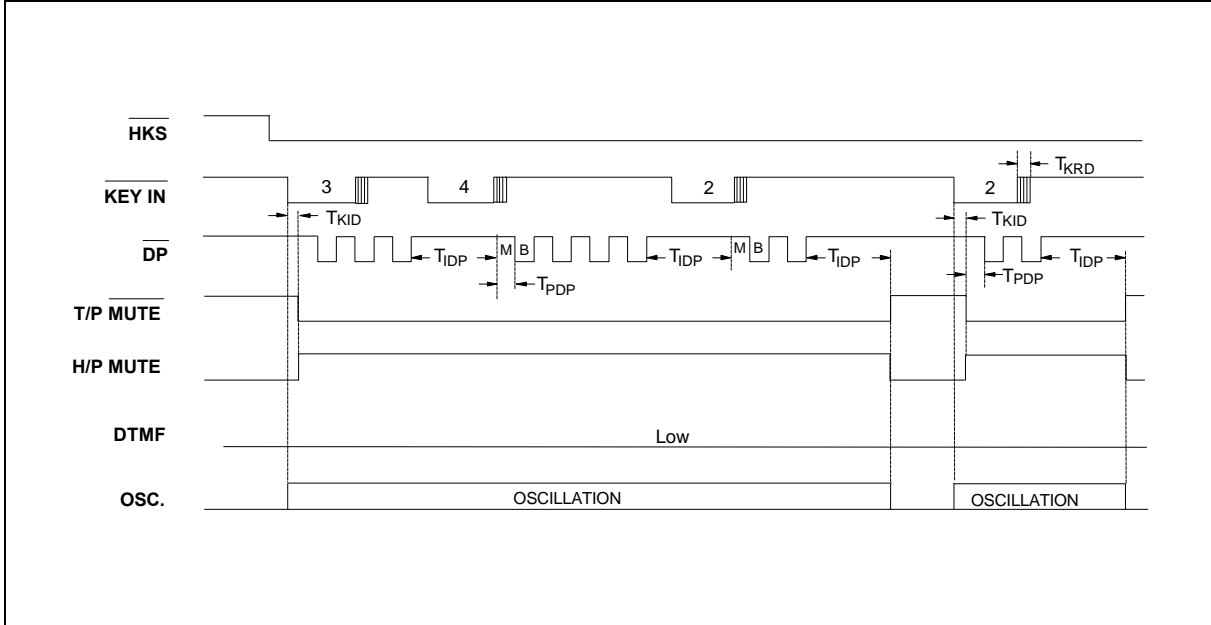
TIMING WAVEFORMS

Figure 1(a) Normal Dialing Timing Diagram
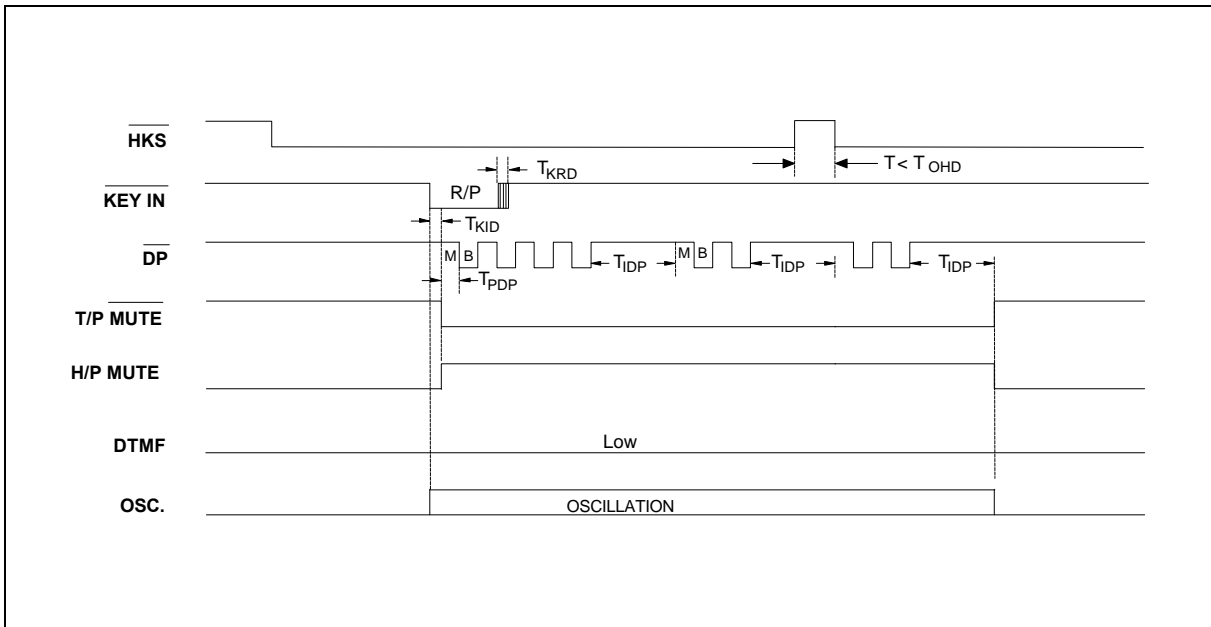

Figure 1(b) Pulse Mode Auto Dialing Timing Diagram
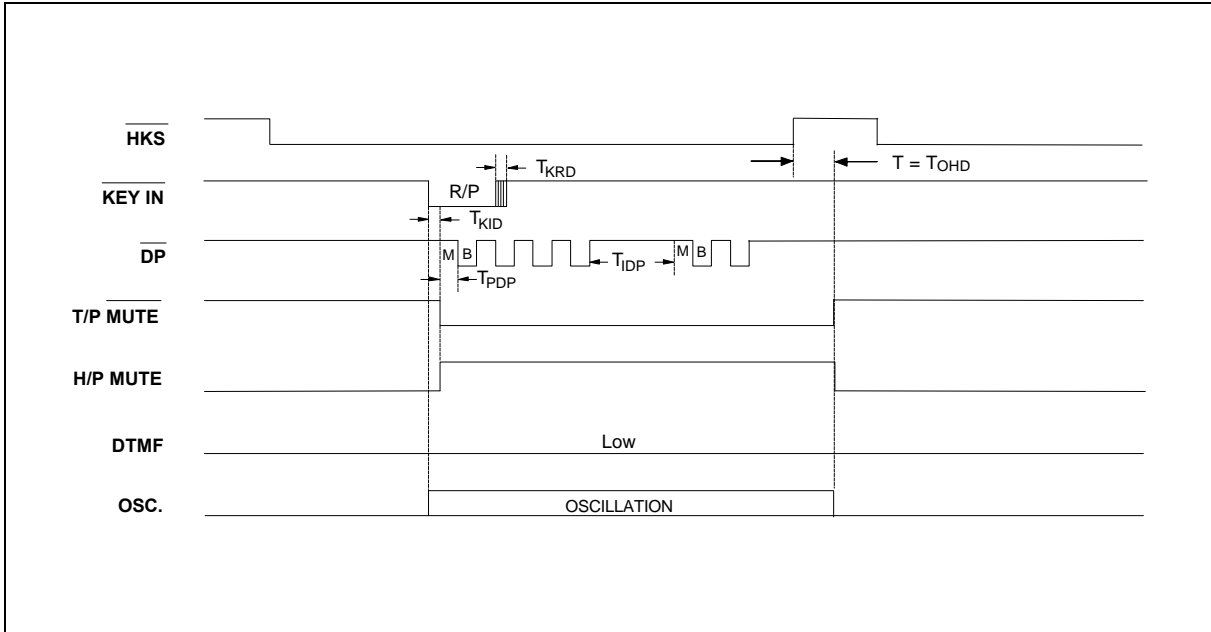
Timing Waveforms, continued

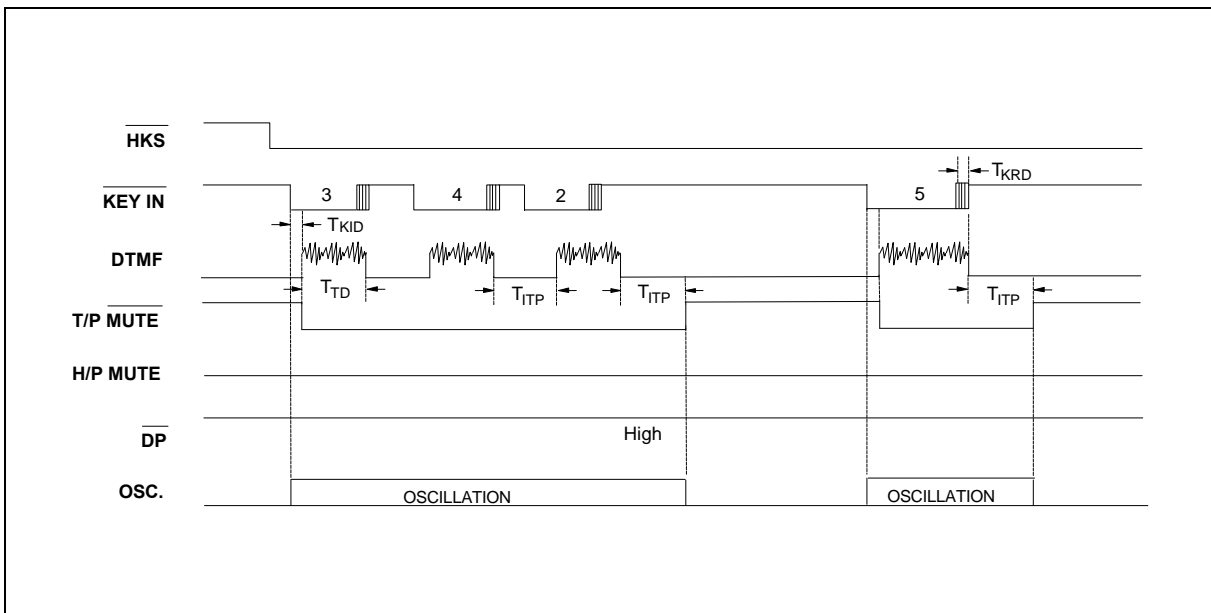Figure 1(c) Pulse Mode Auto Dialing Timing Diagram

Figure 2(a) Tone Mode Normal Dialing Timing Diagram
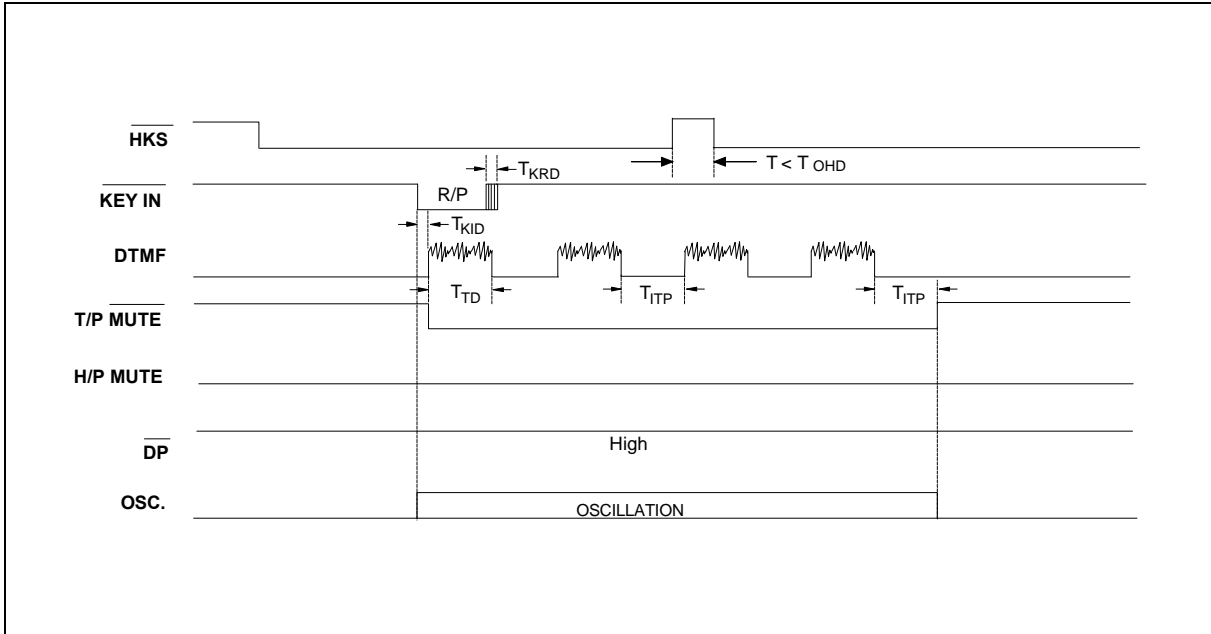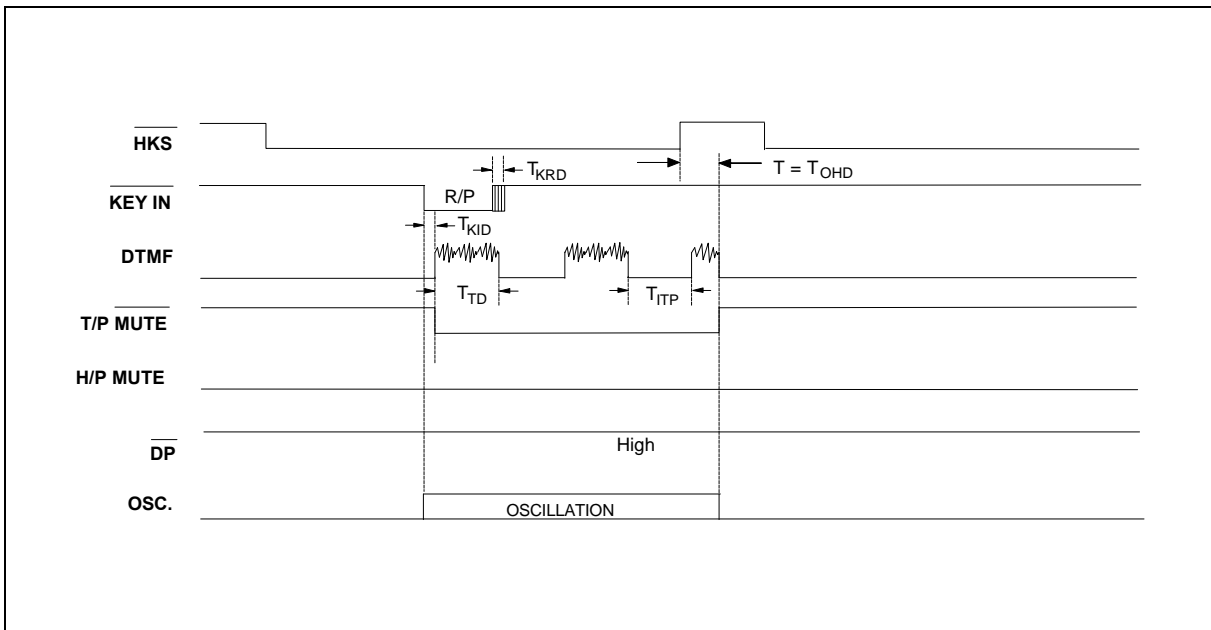
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Timing Waveforms, continued

Figure 2(b) Tone Mode Auto Dialing Timing Diagram

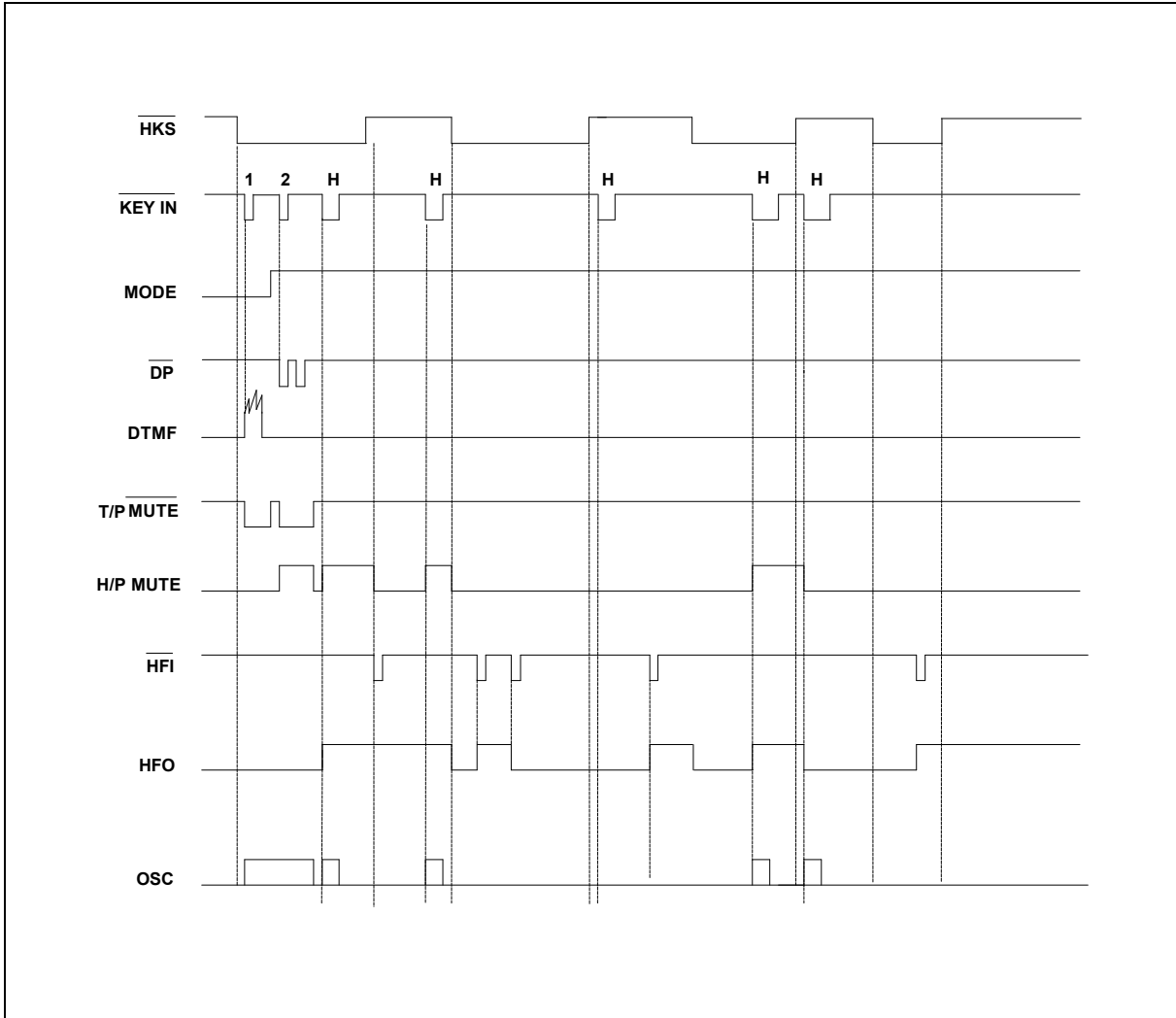Figure 2(c) Tone Mode Auto Dialing Timing Diagram

Timing Waveforms, continued

Figure 3. Handfree Function Timing Diagram

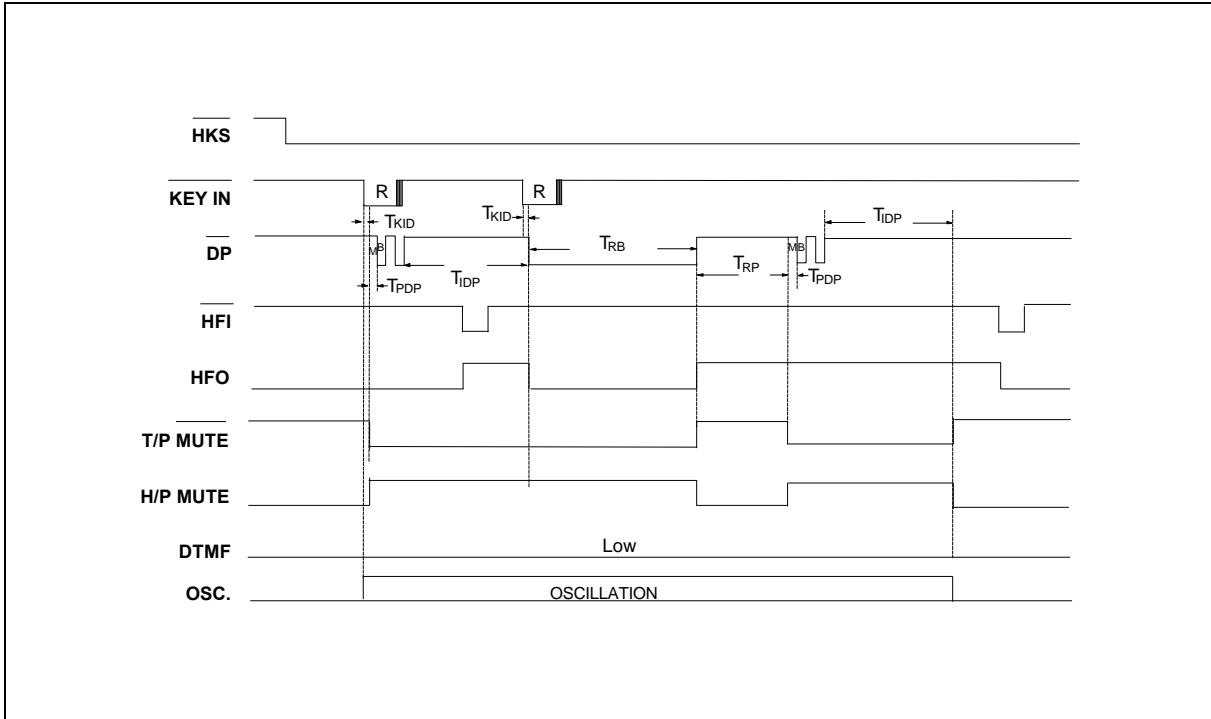Timing Waveforms, continued
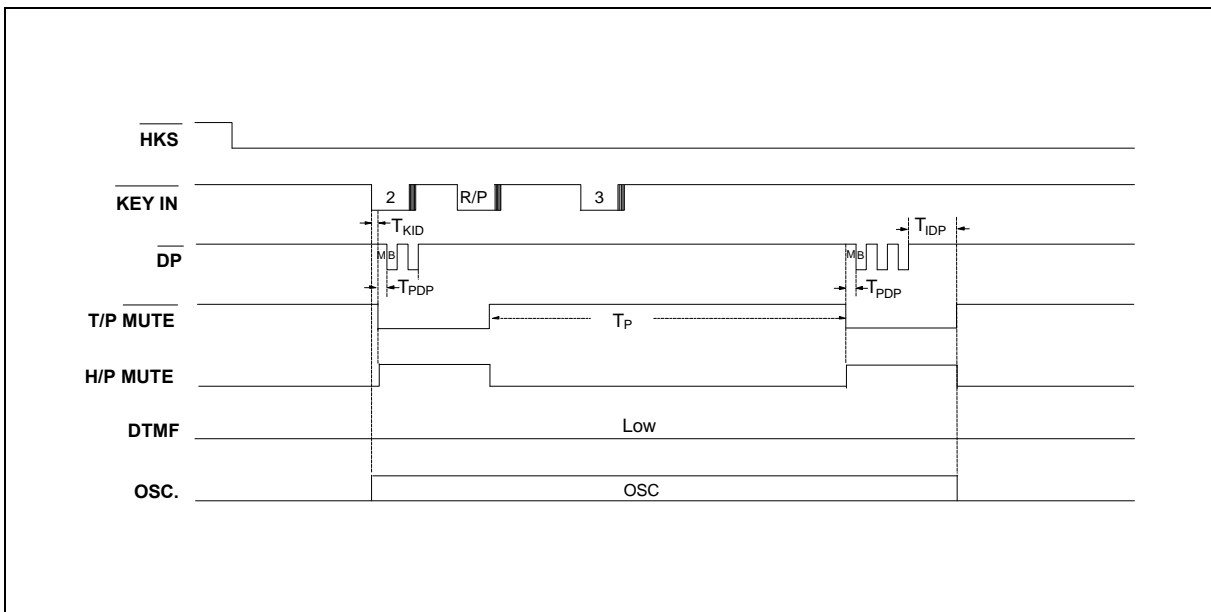

Figure 4. One-key Redial Timing Diagram

Figure 5. Pause Function Timing Diagram
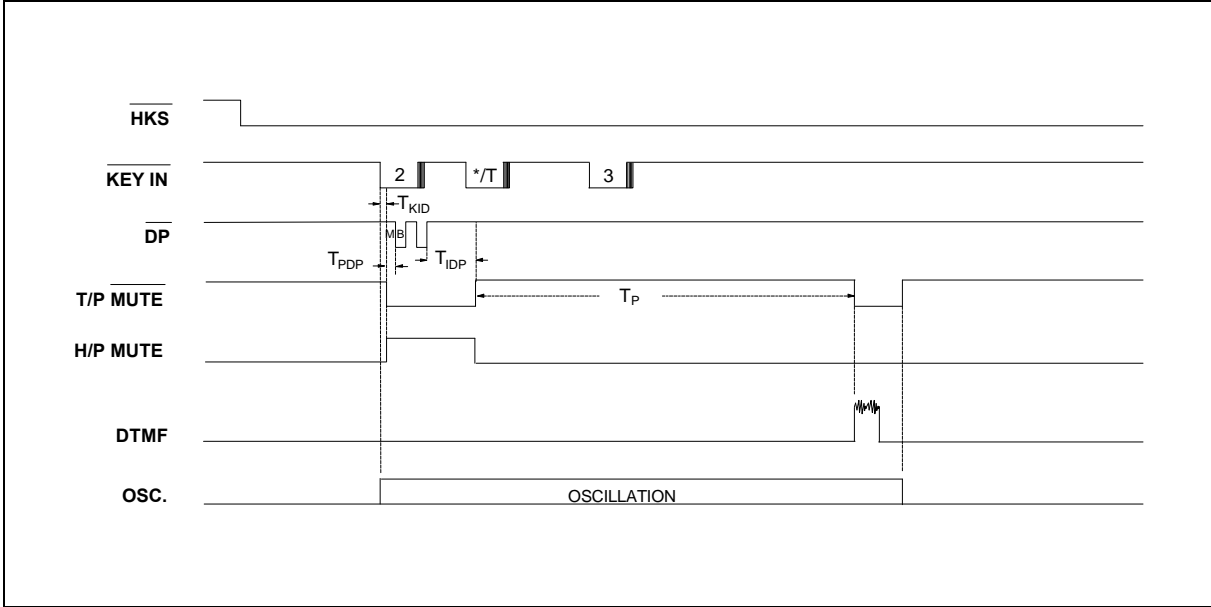
Timing Waveforms, continued

Figure 6. Pulse-to-tone Timing Diagram
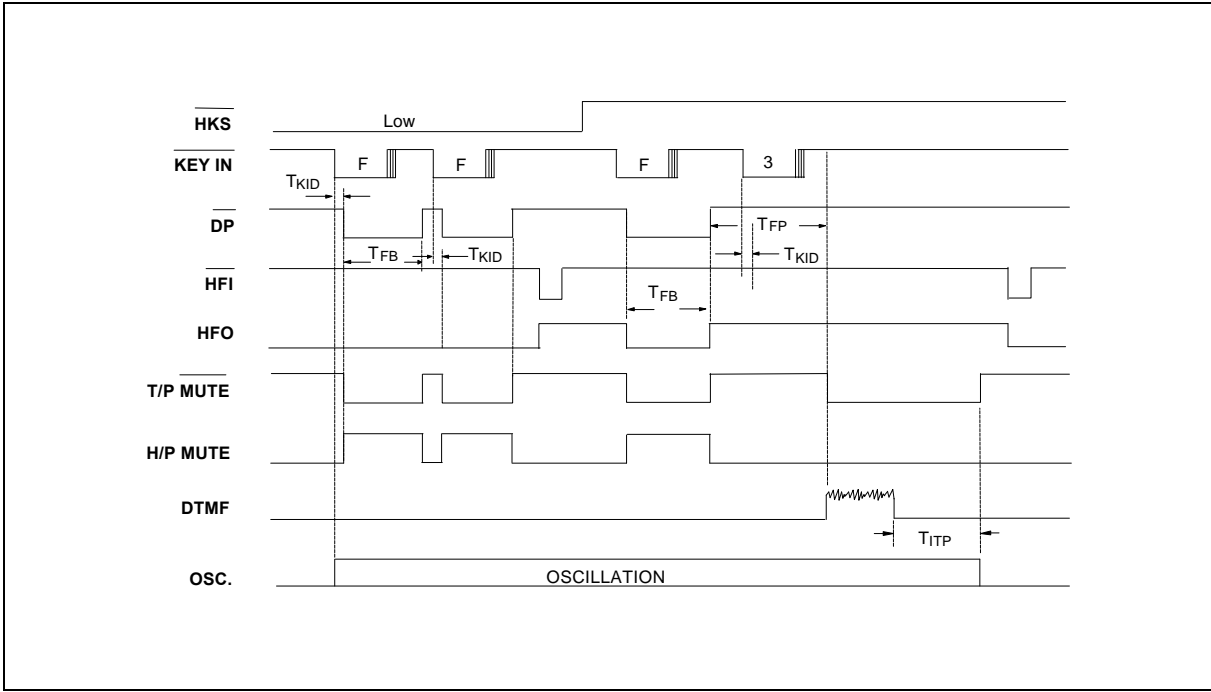

Figure 7. Flash Timing Diagram

W91520N SERIES

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Note: All data and specifications are subject to change without notice.