

WT8076 Passive Infrared (PIR) Controller

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GENERAL DESCRIPTION

The WT8076 is a PIR controller IC which integrates amplifier, window detector and light control functions. The light can be controlled by a Relay or Triac with adjustable delay time. A CDS interface is provided to sense day time and night time. A walk-test fuction helps user while setting up the light set. The line remote control function can switch the light set between auto mode and always-on mode.

FEATURES

- Adjustable detection range
- Adjustable delay time
- CDS interface to differentiate night time and day time
- Walk-test function
- Line remote control to switch auto mode and always on mode
- Relay or Triac output
- High noise immunity
- Low power consumption (operating 500uA, standby 130uA, Typ.)

PIN ASSIGNMENT

WT8076N16P1		WT80	76S16P1	WT8076N18P2			
	$6 \square UOU2$	$GND \square 1$	$16 \square \text{VREF}$		$18 \square UOU2$		
	$5 \square NII2$	$TB \square 2$		NII1 \square 2	17 🗆 NII2		
			$14 \square \text{NII1}$		$16 \square II2$		
	$3 \square CDS$	$TCI \Box 4$	13 🛛 UOU1	VREF 4	$15 \square CDS$		
	$2 \square VDD$	TRIAC 5	$12 \square UOU2$	$GND \Box 5$	14 🗆 VDD		
TB 6 1		$RELAY \sqsubseteq 6$	11 🗋 NII2	TB \Box 6	13 🛛 ND		
	$0 \square \text{ RELAY}$	$ZCD \square 7$	10 🏳 II2	Π 7	12 🗆 WK		
TCI 🗆 8	9 🛛 TRIAC	VDDC 8	$9 \square CDS$	TCI 🗆 8	11 🗖 ZCD		
		L		TRIAC 🗆 9	$10 \square RELAY$		

Ordering Information

PART No.	PACKAGE		
WT8076N16P1	16 Pin P-DIP		
WT8076S16P1	16 Pin SOIC		
WT8076N18P2	18 Pin P-DIP		



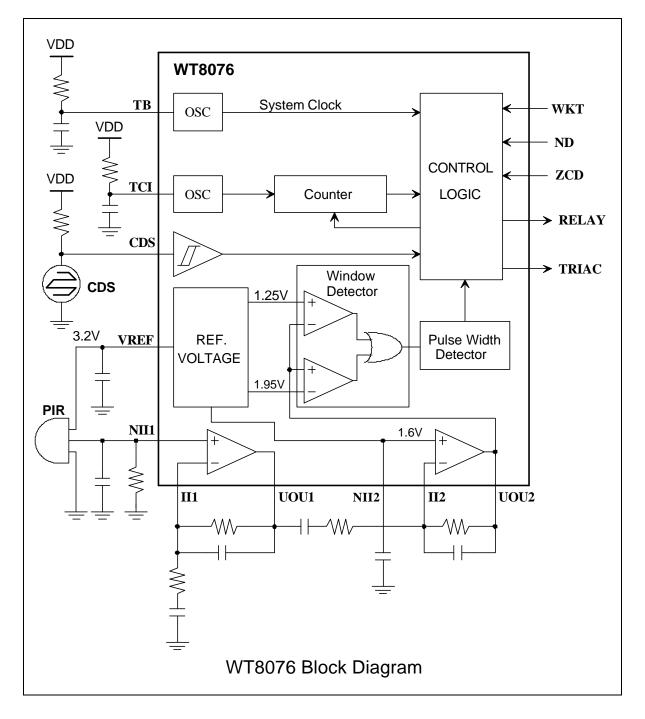
PIN DESCRIPTION

Pin No.		Name	Descriptions		
N16P1	S16P1	N18P1			
1	13	1	UOU1	Output of the first stage OP amplifier.	
2	14	2	NII1	Non-inverting input of the first stage OP amplifier.	
3	15	3	Π1	Inverting input of the first stage OP amplifier.	
4	16	4	VREF	Reference voltage output.	
5	1	5	GND	Ground.	
6	2	6	TB	Time Base. A RC oscillaror is connected on this pin.	
				Typical frequency is 30kHz.	
7	3	7		No connection.	
8	4	8	TCI	Time control input. A RC oscillator is connected on this	
				pin. It controls the delay time of the light.	
9	5	9	TRIAC	Triac control pin. Active low.	
10	6	10	RELAY	Relay control pin. Active hight.	
11	7	11	ZCD	Zero-crossing input. It detects the zero-crossing point of	
				the AC line. The ouput of TRIAC pin is synchronous to this	
				pin.	
*	*	12	WKT	Walk-Test function enable/disable. Internal pull low. The walt-test function is disbled when this pin is high level.	
*	*	13	ND	Night and day detection enable/disable. Internal pull low.	
				The night-and-day detection is disbled when this pin is	
				high level.	
12	8	14	VDD	Power supply.	
13	9	15	CDS	Connect a photo-sensitive resistor to detect night time	
				and day time. If the input level is lower than 1.2V, it is	
				day time. If the input level is higher than 1.9V, it is night	
\vdash				time.	
14	10	16	II2	Inverting input of the second stage OP amplifier.	
15	11	17	NII2	Non-inverting input of the second stage OP amplifier.	
16	12	18	UOU2	Output of the second stage OP amplifier.	

* WT8076N16P1 and WT8076S16P1 : Walk-Test and Night&Day function is always enabled.



BLOCK DIAGRAM





FUNCTIONAL DESCRIPTION

PIR Amplifier

The amplifier has two stages, please see block diagram . The voltage gain and frequency response can be adjusted by the external resisters and capacitors.

Window Detector

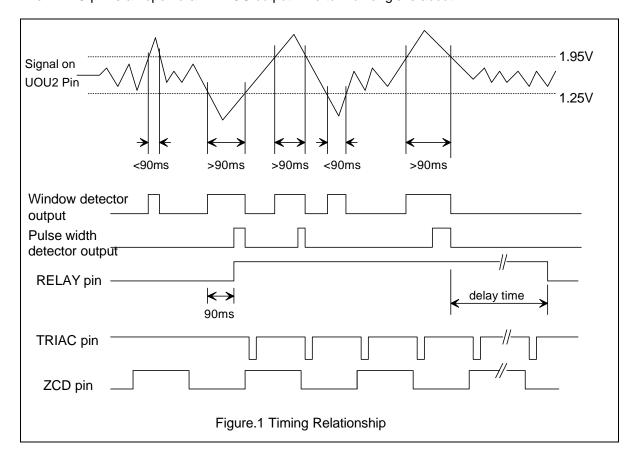
If the amplified PIR signal is between V_H (1.95V) and V_L (1.25V), the signal is ignored and the output of the window detector is low level. If the amplified signal is larger than V_H or smaller than V_L, the output of the window detector is high level.

Pulse Width Detector

The pulse width detector detects the pulse width of the window detecter's output. If the pulse width is less than 90ms ($f_{TB} = 30$ kHz), this pulse is ignored. If the pulse width is larger than 90ms, it will trigger the light to turn on.

Light Control

The light is controlled by the RELAY pin or TRIAC pin. The RELAY pin outputs a high level when a valid pulse is detected. The delay time is determined by the RC on the TCI pin. If another valid pulse occurs during the delay time, the delay time will be reset. Fig. 1 shows the relationships. The TRIAC pin is an open drain NMOS output. The turn on angle is about 24°.





CDS Interface

The CDS interface detects the level on the CDS pin. If the level is higher than 1.95V, it is night time. If the level is lower than 1.25V, it is day time.

Operating Modes

The operating modes of WT8076 are:

(1) Auto mode:

The light will turn on when a valid pulse is detected in the night time.

(2) Always on mode:

If the ND pin is low level, the light is always on in the night time. When it detects day time, the light will be turn off, and back to auto mode.

If the ND pin is high level, the light is always on.

(3) Walk-test mode:

The walk-test mode is active after the power is on. This mode is same as the auto mode except the CDS is ignored. That is, the light will be turn on when a valid pulse is detected no matter it is night time or day time.

Remote Control

To switch auto mode between always on mode, the WT8076 detects the zero crossings of the AC line. If the line switch OFF and ON one time in 3 seconds, it toggles the auto mode and always on mode.

Time Base

The RC oscillator on the TB pin generates the system clock. The frequency is 30kHz typically (R =30Kohm, C=1000pF).

The following parameters are decided by the time base :

(1) Trigger angle of the TRIAC (24^o, if time base is 30kHz).

- (2) Pulse width (90ms, if time base is 30kHz).
- (3) Remote control switch time.
- (4) Disable PIR after the light is off.
- (5) Total walk-test time.

The RC oscillator on the TCI pin sets the delay time. Figure 2 shows the relationship betwteen delay time and the R (C=1000pF).

ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings

ltem	Symbol	Ratings	Uit
supply voltage	Vdd	6	V
Input Voltage	Vin	-0.5 to V _{DD} +0.5	V
Operating Temperature	Topr	-25 to 70	O ^o
StorageTemperature	Тѕтс	-65 to 150	°C
Power Dissipation	PD	500	mW

Note: Stress above those conditions listed may cause permente damage to the device.

Electrical Characteristics (VDD=5.0V, TA=25°C)

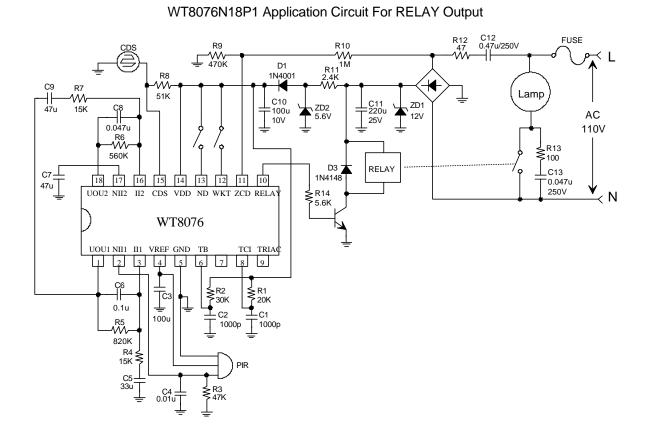
ltem	Symbol	Test Condition	Min	Тур	Max	Unit
Operating Voltage	Vdd		4.5	5	5.5	V
Standby Current	Idds		-	130	600	mA
Operating Current	Iddo	No load on output.	-	500	1000	mA
Reference Voltage	VREF		3	3.2	3.4	V
Ripple of VREF			-	-	0.5	mV
Time Base Frequency	fтв	R=30K,C=1000p	28	30	32	kHz
CDS VT+	VT+		-	1.95	2.1	V
CDS VT-	VT+		1	1.25	-	V
TCI Frequency	fтсı		-	-	50k	Hz
TRIAC pin Sink Current	Iol	Vol = 1.5V	-	-	15	mA
RELAY pin Source Current	Іон	Vон = 3.5V	-	-	5	mA

Characteristics of OP Amplifier (VDD=5.0V, TA=25°C)

ltem	Symb ol	Test Condition	Min	Тур	Max	Unit
Input Bias Current	Ibias		-	-	10	nA
Input Offset Current	Ioffset		-	-	10	nA
Input Offset Voltage	Voffset		-	-	5	mV
Output Swing Voltage	Vswing	R∟=470K	4	-	-	V
Output Source Current	Isource		7	-	-	uA
Output Sink Current	Isink		3.5	-	-	mA
Differential Gain	Av		-	100	-	dB
Commom Mode Rejection Ration	CMRR		-	65	-	dB
Power Supply Rejection Ration	PSRR		-	80	-	dB



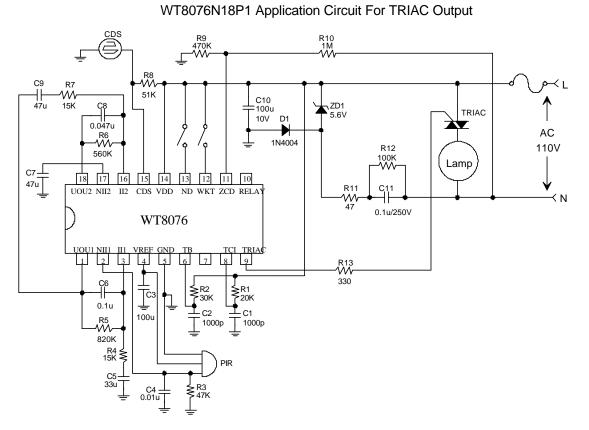
TYPICAL APPLICATION CIRCUIT



Note:

- (1) To adjust delay time, change R1 value. (R1=20K, delay time is about 10 sec)
- (2) For different CDS, R8 value should be adjusted.





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