

X88C64 SLIC® E²

ing Gate Technology. The X88C64 features a Multiplexed Address and Data bus allowing direct interface to a variety of popular single-chip microcontrollers operating in expanded multiplexed mode without the need for additional interface circuitry.

The X88C64 is internally configured as two independent 4K x 8 memory arrays. This feature provides the ability to perform nonvolatile memory updates in one array and continue operation out of code stored in the other array; effectively eliminating the need for an auxiliary memory device for code storage.

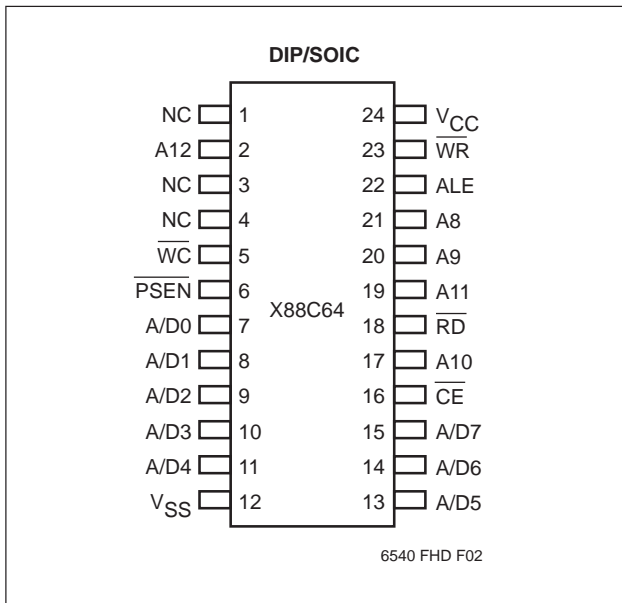
To write to the X88C64 SLIC E², a three-byte command sequence must precede the byte(s) being written. This sequence called Software Data Protection

prevents the loss of data or program information due to inadvertant write cycles during power-up or power-down. The X88C64 SLIC E² also provides a second generation software data protection scheme called Block Protect.

Block Protect can provide write lockout of the entire device or selected 1K blocks. There are eight 1K x 8 blocks that can be write protected individually in any combination required by the user. Block Protect, in addition to Write Control input, allows the different segments of the memory to have varying degrees of alterability in normal system operation.

For further information on the X88C64 hardware interface, consult the X88C64 Data Sheet.

PIN CONFIGURATION



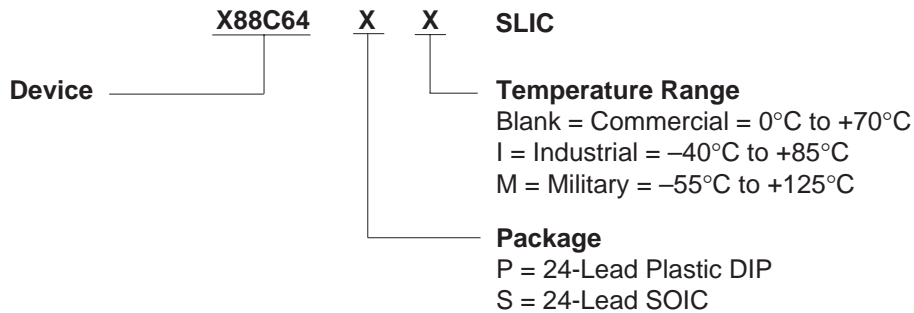
PIN NAMES

Symbol	Description
ALE	Address Latch Enable
A/D ₀ –A/D ₇	Address Inputs/Data I/O
A ₈ –A ₁₂	Address Inputs
\overline{RD}	Read Input
\overline{WR}	Write Input
\overline{PSEN}	Program Store Enable Input
\overline{CE}	Chip Enable
\overline{WC}	Write Control
V _{SS}	Ground
V _{CC}	Supply Voltage

6540 PGM T01

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ORDERING INFORMATION



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US. PATENTS

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LIFE RELATED POLICY

In situations where semiconductor component failure may endanger life, system designers using this product should design the system with appropriate error detection and correction, redundancy and back-up features to prevent such an occurrence.

Xicor's products are not authorized for use as critical components in life support devices or systems.

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.