

Z89135

LOW-COST DUAL-PROCESSOR DTAD CONTROLLERS

FEATURES

 Part
 Z8 ROM Number
 Z8 RAM*
 Speed (KBytes)

 Z89135
 24
 236
 20

*General-Purpose

- Part DSP ROM DSP RAM Speed Number (Words) (Words) (MHz)

 Z89135 6K 512 20
- 68-Pin PLCC Package
- 4.75- to 5.25-Volt Operating Range
- Low-Power Consumption (200 mW Typical)
- 0°C to +55°C Temperature Range
- 25 Expanded Register Files

- 47 Input/Output Lines
- Six Vectored, Prioritized Z8 Interrupts with Programmable Polarity
- Three Vectored, Prioritized DSP Interrupts with Programmable Polarity
- Two Analog Comparators
- Two Programmable Z8 8-Bit Counter/Timers, Each with Two 6-Bit Programmable Prescaler
- Watch-Dog Timer /Power-On Reset
- On-Chip Oscillator that Accepts a Crystal,
 Ceramic Resonator, LC, RC, or External Clock Drive
- RAM and ROM Protect, Low-EMI Option

GENERAL DESCRIPTION

Zilog's Digital Voice Processor Controller family combines a Z8® microcontroller and a DSP processor on-chip for a cost-effective turnkey system in digital telephone answering devices and other voice processing applications.

The dual-processor architecture is loosely coupled by mailbox registers and an interrupt system, enabling DSP or Z8 programs to be directed by events in each other's domain.

The Z8 microcontroller uses an expanded register file to allow access to register-mapped peripheral and I/O circuits for programming versatility.

The 16-bit DSP processor features a 24-bit ALU and accumulator with single-cycle instructions, providing the algorithm processing power necessary for telephone voice quality.

The Z89135 device offers a half-flash 8-bit A/D converter with up to 128 kHz sample rate and a 10-bit Pulse-Width modulator (PWM) D/A converter, eliminating the need for an external CODEC.

Notes:

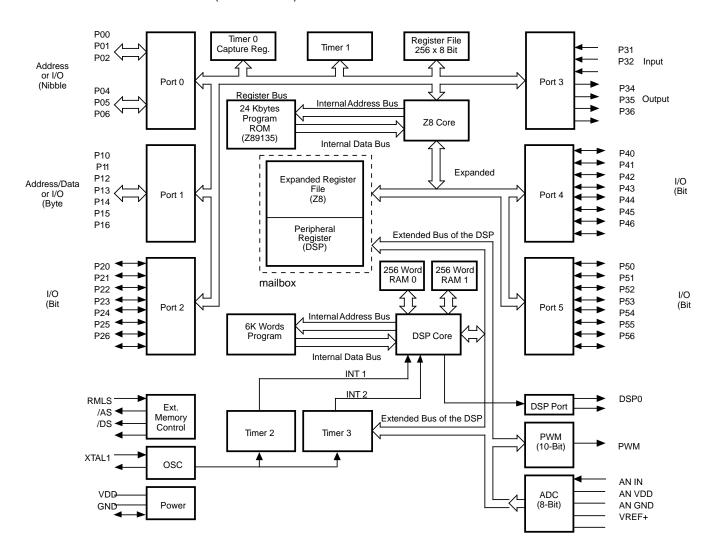
All Signals with a preceding front slash, "/", are active Low, e.g.: B//W (WORD is active Low); /B/W (BYTE is active Low, only).

Power connections follow conventional descriptions below:

Connection	Circuit	Device
Power Ground	V _{cc} GND	$egin{array}{c} egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}$



GENERAL DESCRIPTION (Continued)



Z89135 Functional Block Diagram